



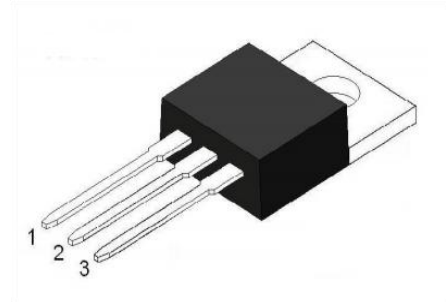
1.Features

- Output Current up to 1.5A
- Output Voltages of 5.0V
- Thermal Overload Protection
- Short Circuit Protection
- Output Transistor Safe Operating area (SOA)Protection

2.Mechanical Data

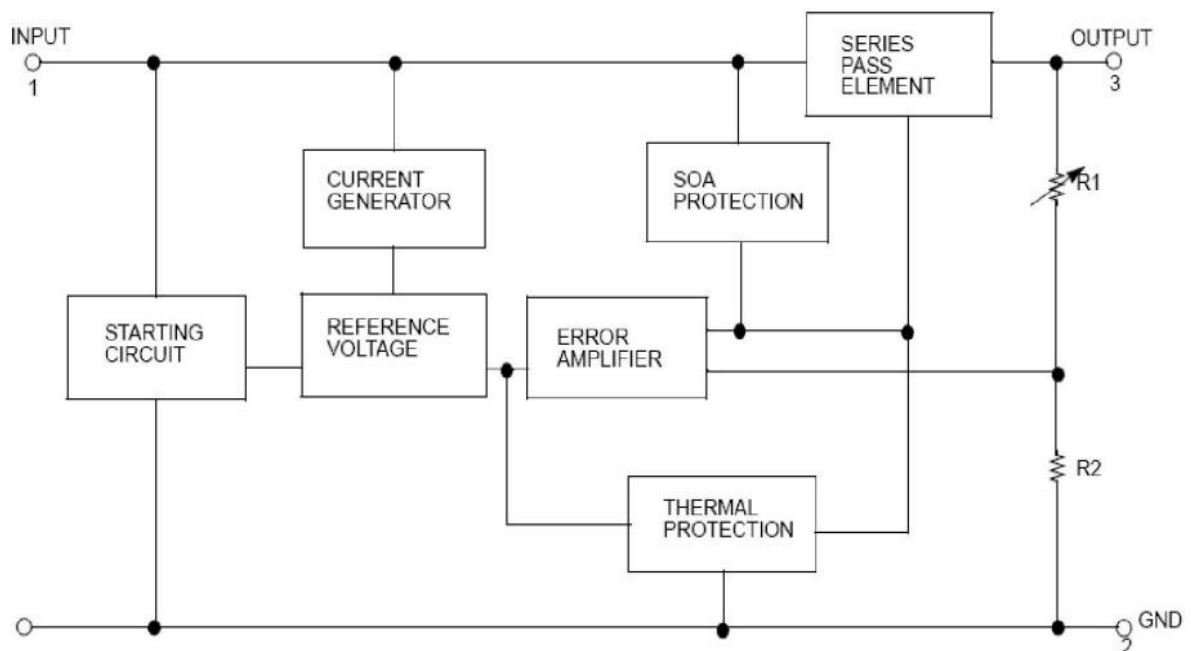
- Case:Molded Plastic,TO-220;
- Epoxy:UL 94V-0 rate flame retardant
- Terminals:Plated Leads Solderable per MIL-STD-750,Method-2026.
- Marking: marked on body.
- Mounting Position : Any.

TO-220



1.INPUT
2.GND
3.OUTPUT

3.Internal Block Diagram



**4.Maximum Ratings and Electrical Characteristics**

Rating at 25℃ Cambient temperature unless otherwise specified

Parameters	Symbol	Value	Unit
Input Voltage	V_{IN}	30	V
Out put Voltage	V_O	5	V
Continuous total dissipation	P_D	1.25	W
Operating Temperature Range	T_{OPR}	0~+125	℃
Storage Temperature Range	T_{STG}	-55~+150	℃

5.Electrical Characteristics (TA=25℃ unless otherwise noted)(Refer to test circuit , $I_O = 0.75A$, $V_I = 10V$, $CI = 0.33\mu F$, $C_O = 0.1\mu F$, unless otherwise specified)

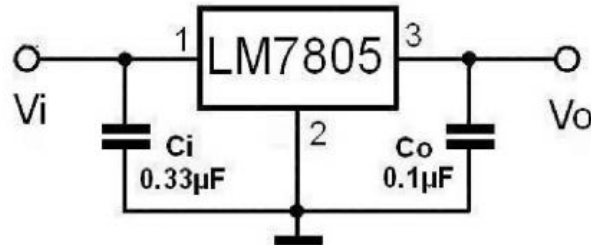
Parameters	Symbol	Cindition	Min	TYP	Max	Unit
Output Voltage	V_O	$I_O = 5 \sim 1.5A$	4.8	5	5.2	V
		$V_I = 7 \sim 20V$, $I_O = 5 \sim 1.5A$	4.8	5	5.25	V
Line Regulation	ΔV_O	$V_I = 7 \sim 25V$, $T_J = 25^\circ C$	-	-	100	mV
		$V_I = 8 \sim 12V$, $T_J = 25^\circ C$	-	-	50	mV
Load Regulation	ΔV_O	$V_I = 5m \sim 1.5V$, $T_J = 25^\circ C$	-	-	100	mV
		$V_I = 0.25 \sim 0.75V$, $T_J = 25^\circ C$	-	-	50	mV
Quiescent Current	I_Q	$T_J = 25^\circ C$	-	-	8	mV
Quiescent Current Change	ΔI_Q	$I_O = 5mA \sim 1.5A$	-	-	0.5	mV
		$V_I = 7 \sim 25V$	-	-	1.3	mV
Output Voltage Drift	$\Delta V / \Delta T$	$I_O = 5mA$	-	-0.8	-	mV/℃
Output Noise Voltage	V_N	$10Hz \leq f \leq 100KHz$	-	42	-	μV
Ripple Rejection	RR	$f = 120Hz$, $V_I = 8$ to $18V$	62	-	-	dB
Dropout Voltage	V_d		-	2	-	V
Short Circuit Current	I_{SC}	$T_J = +25^\circ C$, $V_I = 30V$	-	230	-	mA
Peak Current	I_{PK}		-	2.2	-	A

Notes:

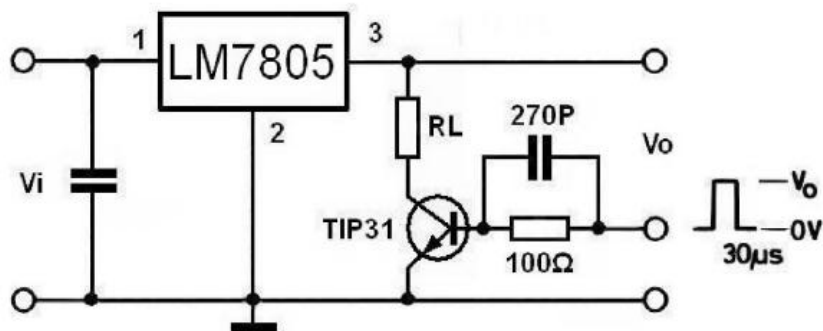
*Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.



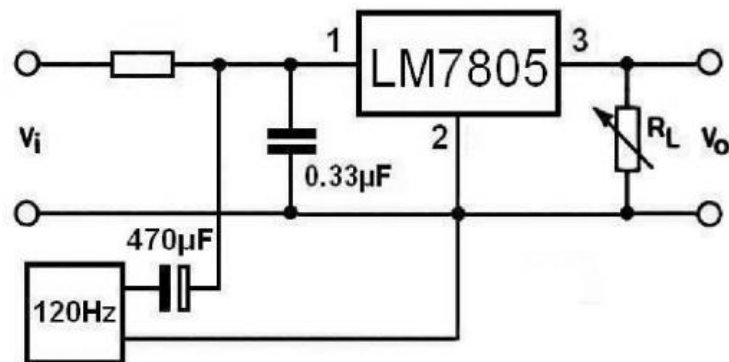
6.Test Circuits



DC Parameter



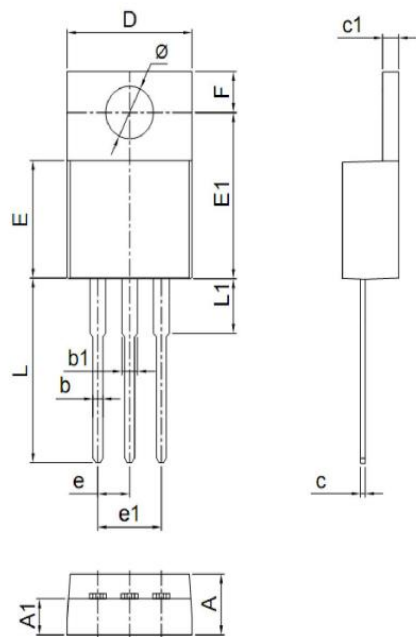
Load Regulation



Ripple Rejection

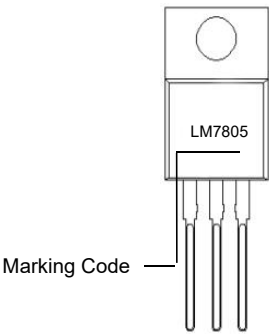
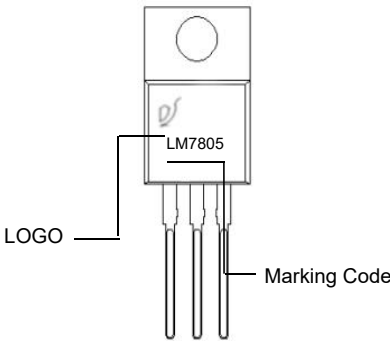


7.Test circuits



Dimensions	Inches		Millimeters	
	Min	Max	Min	Max
A	0.169	0.185	4.30	4.70
A1	0.098	0.110	2.50	2.80
b	0.028	0.035	0.70	0.90
b1	0.047	0.059	1.20	1.50
c	0.012	0.024	0.30	0.60
c1	0.045	0.055	1.15	1.40
D	0.390	0.402	9.90	10.20
E	0.335	0.354	8.50	9.00
E1	0.472	0.492	12.00	12.50
e	0.094	0.104	2.40	2.65
e1	0.191	0.209	4.85	5.30
F	0.102	0.110	2.60	2.80
L	0.520	0.543	13.20	13.80
L1	0.150	0.165	3.80	4.20
Ø	0.142	0.157	3.60	4.00

8. Part Marking System



9. Package Information

Package	Packing Type	Quantity(pcs)
TO-220	Tube	50



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