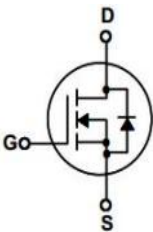
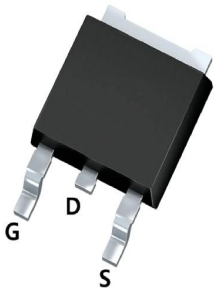




1.Features

- $R_{DS(ON)} \leq 115m\Omega$ @ $V_{GS}=10V$, $I_D=8A$
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Advanced trench technology
- Lead free product is acquired

TO-252



1. Gate
2. Drain
3. Source

2.Mechanical Data

- Case:Molded Plastic,TO-252;
- Epoxy:UL 94V-0 rate flame retardant
- Terminals:Plated Leads Solderable per MIL-STD-750,Method-2026.
- Marking: marked on body.
- Mounting Position : Any.

3.Maximum Ratings and Electrical Characteristics

Rating at 25°C Cambient temperature unless otherwise specified

Characteristics		Symbol	Ratings	Unit
Drain-Source Voltage		V_{DS}	100	V
Drain Current Continuous	$T_C=25^{\circ}C$	I_D	16	A
	$T_C=100^{\circ}C$		12.8	
Drain Current Pulsed(Note 1)		I_{DM}	64	A
Single Pulsed Avalanche Energy		E_{AS}	2	mJ
Gate-Source Voltage		V_{GS}	± 20	V
Power Dissipation($T_C=25^{\circ}C$)		P_D	78	W
Maximum Junction Temperature		T_J	150	$^{\circ}C$
Storage Temperature Range		T_{stg}	-55 to +150	$^{\circ}C$
Thermal Resistance, Junction-to-Case		$R_{\theta JC}$	1.6	$^{\circ}C/W$



Characteristics	Symbol	Test conditions	Min	TYP	Max	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V,I _D =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V,V _{GS} = 0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = 20V	-	-	100	nA
Gate-Source Leakage Current		V _{GS} = -20V	-	-	-100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS (th)}	V _{GS} = V _{DS} , I _D =250μA	1	2	3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =8A	-	-	115	mΩ
		V _{GS} =4.5V, I _D =8A	-	-	135	mΩ
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DB} =25V	-	625	-	pF
Output Capacitance	C _{oss}	V _{GS} =0V	-	25	-	
Reverse Transfer Capacitance	C _{rss}	f=1.0MHZ	-	20	-	
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V, V _{GS} =10V R _G =3Ω; I _D =16A	-	6.3	-	ns
Turn-on Rise Time	t _r		-	4	-	
Turn-off Delay Time	t _{d(off)}		-	21	-	
Turn-off Fail Time	t _f		-	3.9	-	
Total Gate Charge	Q _g	V _{DS} =50V, V _{GS} =10A I _D =8A	-	20.5	-	nc
Gate-Source Charge	Q _{gs}		-	2.2	-	
Gate-Drain Charge	Q _{gd}		-	3.8	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	I _S =16A,V _{GS} =0V	-	-	1.4	V
Continuous Source Current	I _S		-	-	16	A
Pulsed Source Current	I _{SM}		-	-	64	
Reverse Recovery Time	T _{rr}	I _F =16A,	-	121	-	ns
Reverse Recovery Charge	Q _{rr}	dI/dt=100A/μS	-	705	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Essentially independent of operating temperature



4. Rating And Characteristic Curves

Fig.1 Output Characteristics

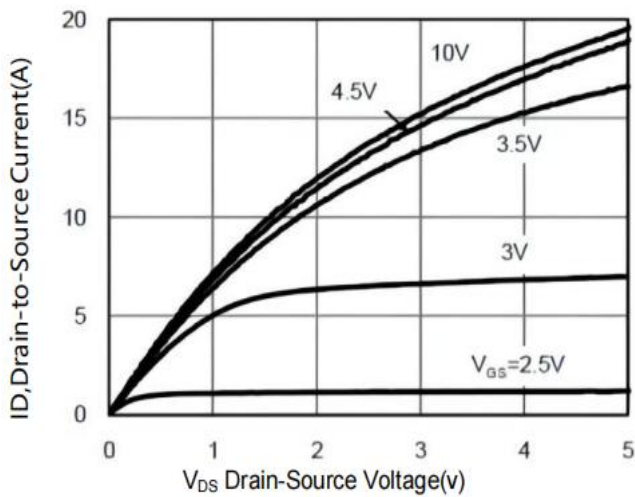


Fig.2 Transfer Characteristics

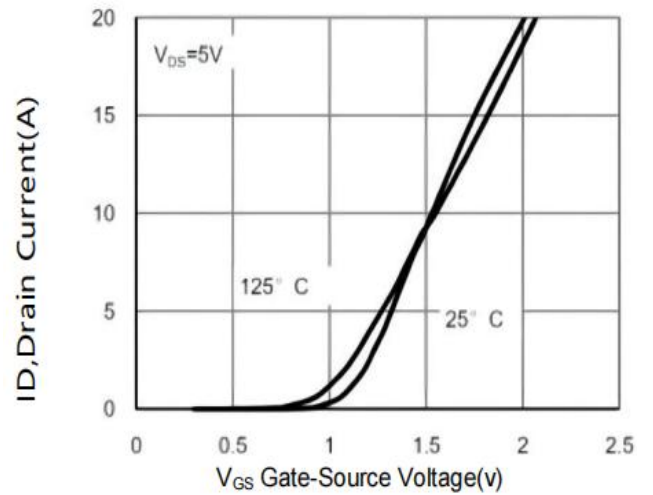


Fig.3 On-Resistance versus Drain Current

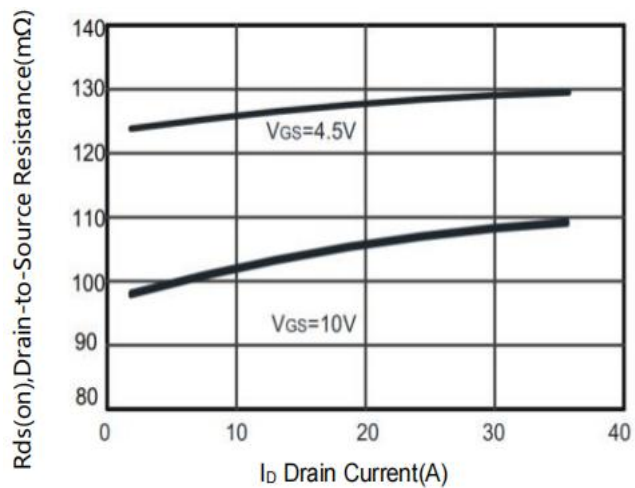


Fig.4 Diode forward voltage versus Current

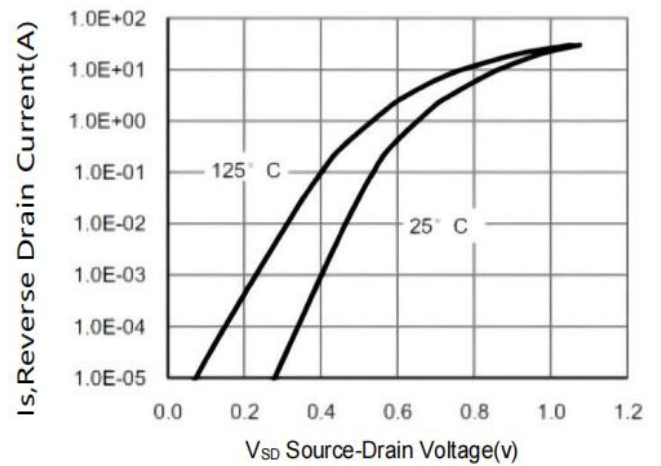


Fig.5 Typical Capacitance versus VDS

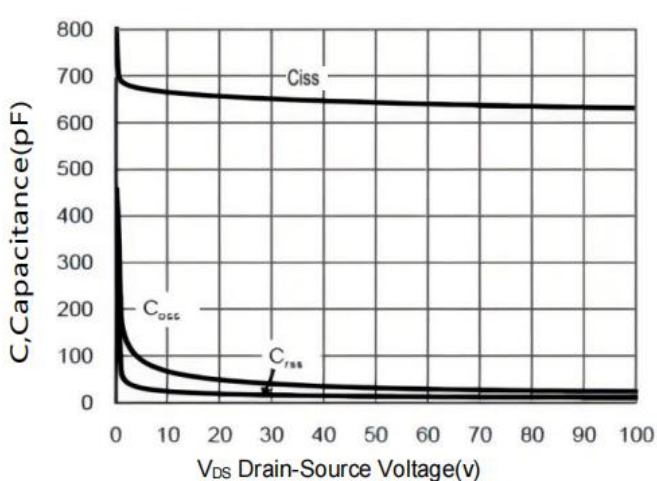


Fig.6 Typical Gate Charge versus VGS

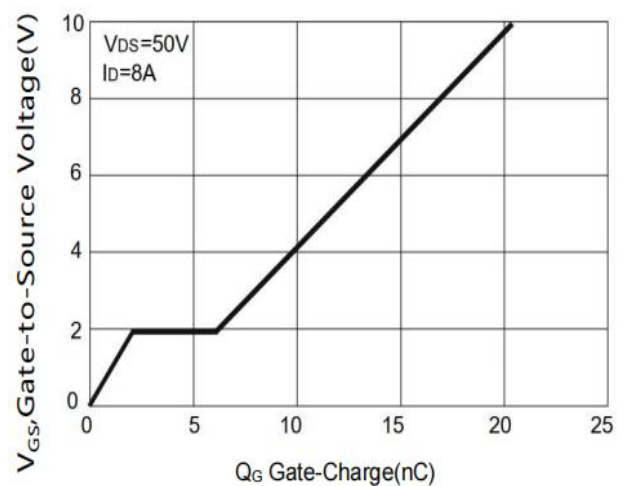




Fig.7 BV_{DSS} Variation with Temperature

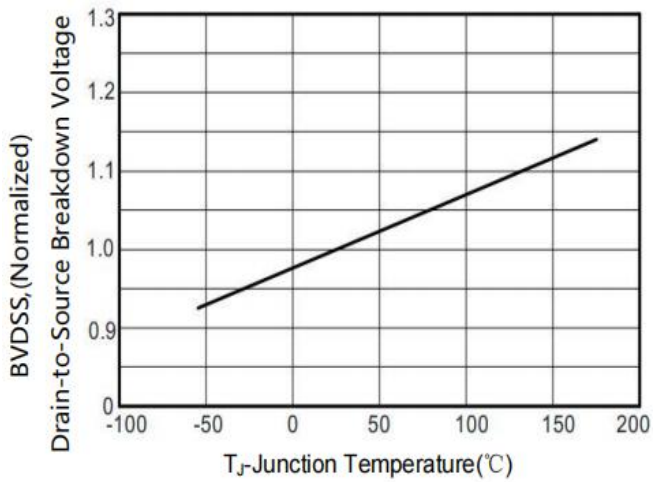


Fig.8 On-Resistance Variation with Temperature

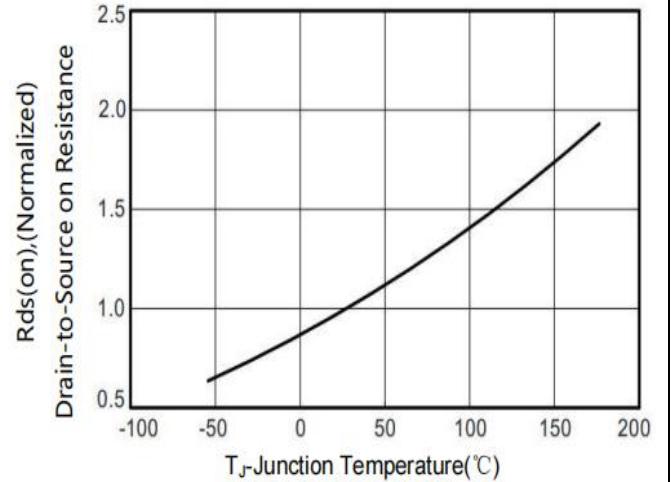


Fig.9 Maximum Safe Operating Area

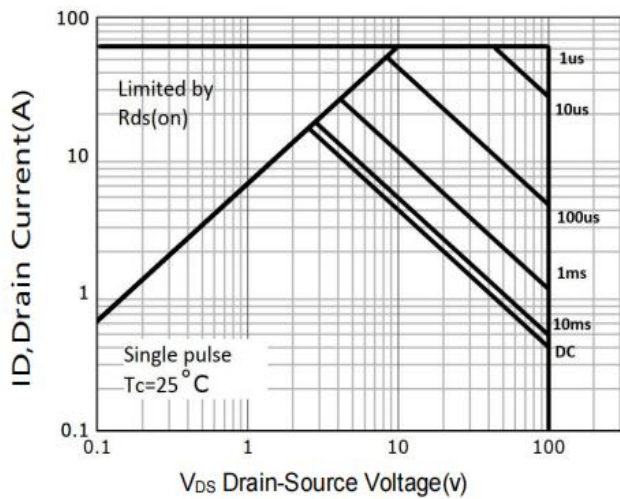
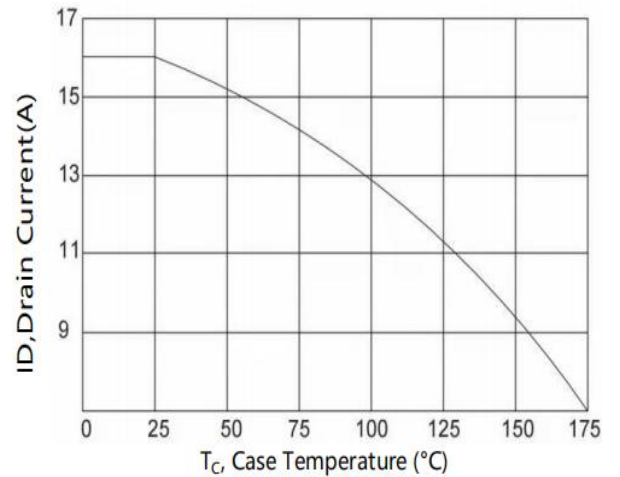
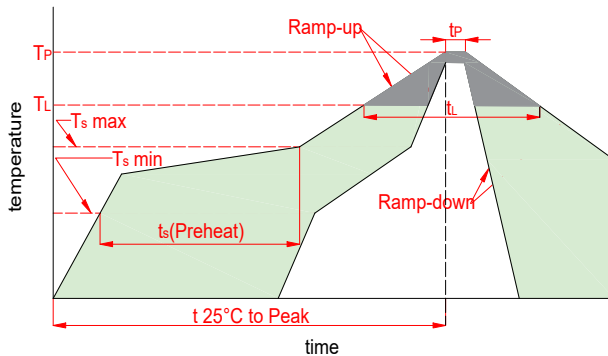


Fig.10. Drain Current versus Case Temperature



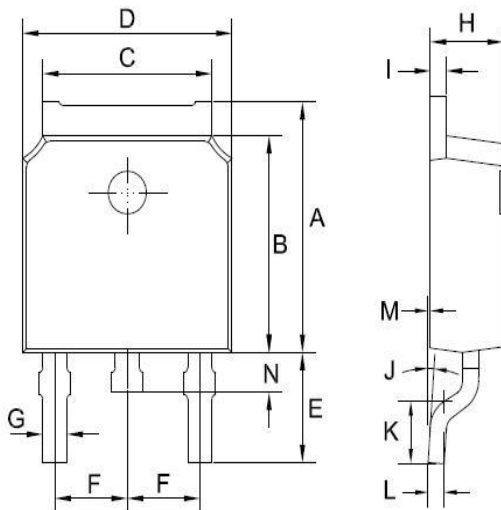


5. Soldering Parameters



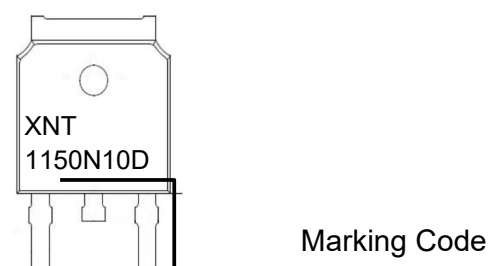
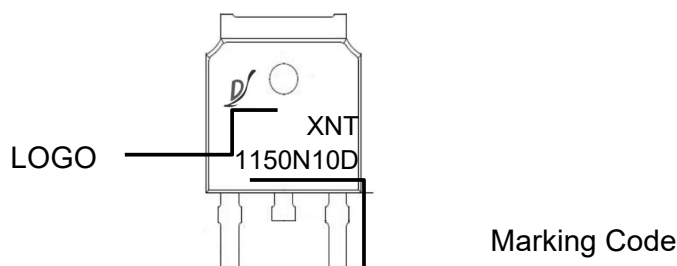
Reflow Condition		Lead-free
Pre Heat	Temp. min(T_s (min))	150℃
	Temp. max(T_s (min))	200℃
	Time(min to max)(t_s)	60~180s
Aver. ramp up rate(Liquidus Temp.)(T_L)to peak		3℃/s max
T_s (max) to T_L -Ramp-up Rate		3℃/s max
Reflow	Temp.(T_L)(Liquidus)	217℃
	Temp.(t_L)(Liquidus)	60~150s
Peak Temp.(T_P)		260 ^{+0/-5} ℃
Time within actual peak Temp.(t_P)		30s max
Ramp-down Rate		6℃/s max
Time 25℃ to peak Tempe.(T_P)		8 minutes max
Do not exceed		260℃

6.Dimensions



Dimensions	Inches		Millimeters	
	Min	Max	Min	Max
A	0.270	0.285	6.85	7.25
B	0.232	0.248	5.90	6.30
C	0.202	0.218	5.13	5.53
D	0.252	0.268	6.40	6.80
E	0.114	0.130	2.90	3.30
F	0.086	0.094	2.19	2.39
G	0.018	0.033	0.45	0.85
H	0.087	0.094	2.20	2.40
I	0.016	0.024	0.41	0.61
J	0°	8°	0°	8°
K	0.057	0.073	1.45	1.85
L	0.016	0.024	0.41	0.61
M	0.000	0.005	0.00	0.12
N	0.024	0.039	0.60	1.00

7. Part Marking System

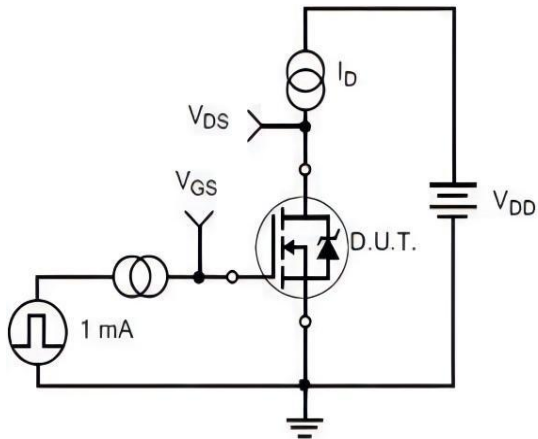


8. Package Information

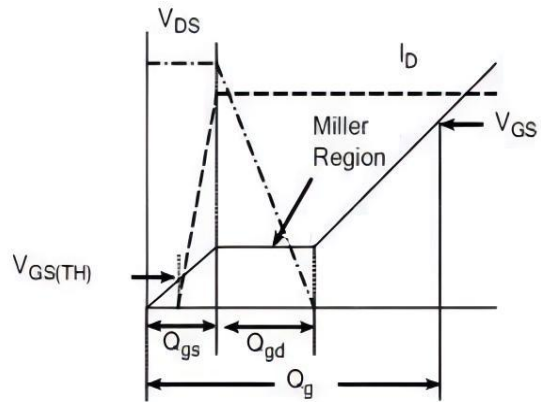
Package	Part Number	Marking Code
TO-252	2500	XNT1150N10D



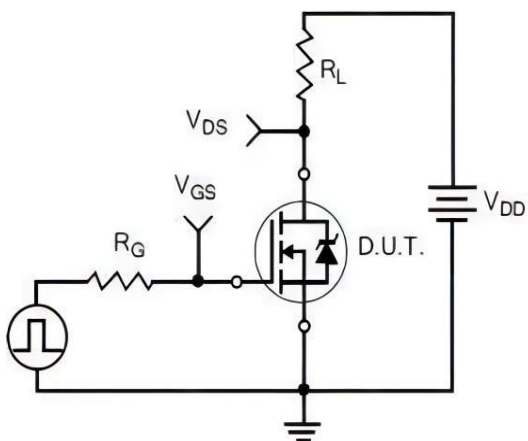
9. Test circuits



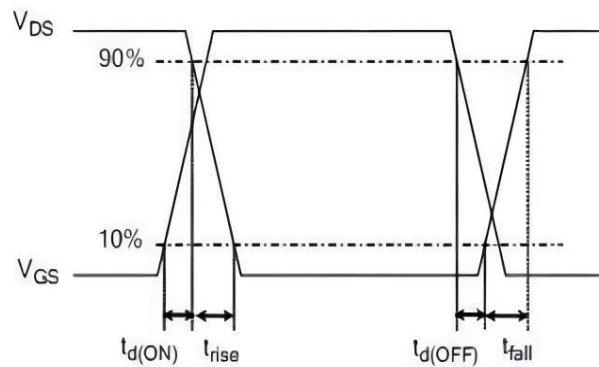
Gate Charge Test Circuit



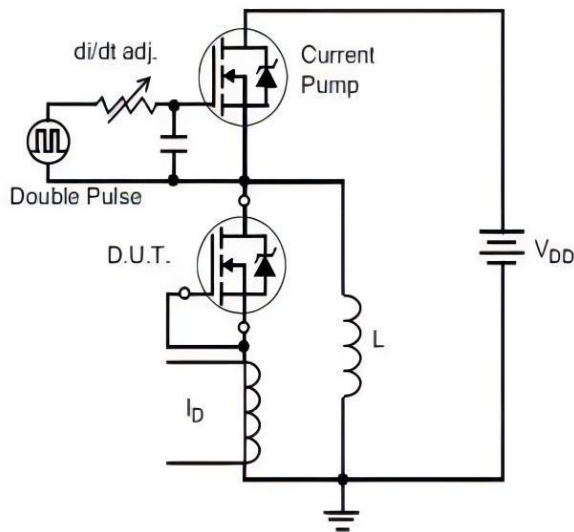
Gate Charge Waveform



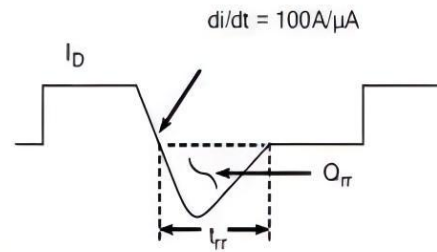
Resistive Switching Test Circuit



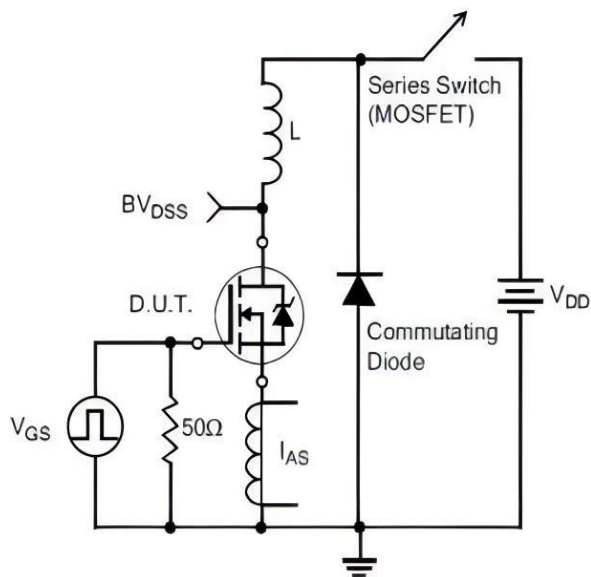
Resistive Switching Waveforms



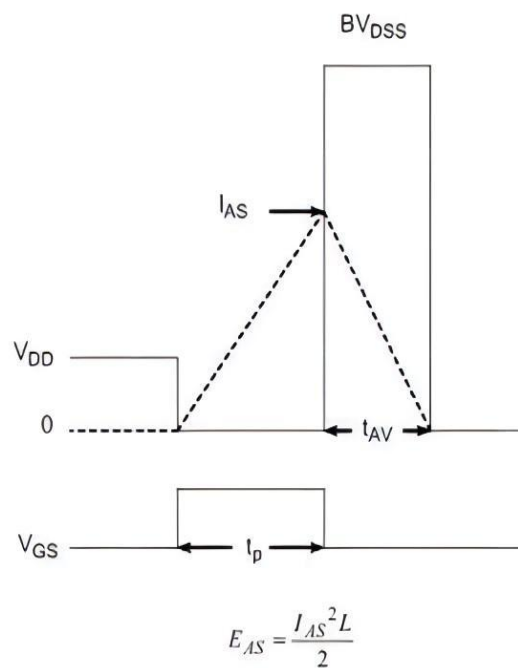
Diode Reverse Recovery Test Circuit



Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms



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