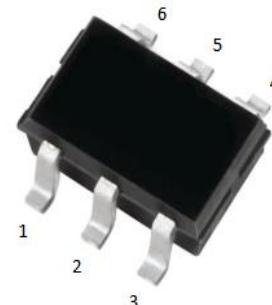




1. Features

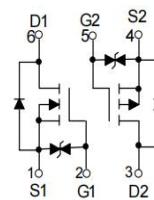
- High-Side Switching
- Low Threshold
- Fast Switching Speed
- APPLICATION:
Drivers:Relays, Solenoids, Memories
Battery Operated Systems
Power Supply Converter Circuits
Load/Power Switching Cell Phones, Pagers

SOT-363



2. Mechanical Data

- Case:Molded Plastic,SOT-363 .
- Epoxy:UL 94V-0 rate flame retardant.
- Terminals:Plated Leads Solderable per MIL-STD-750,Method-2026.
- Marking:.75
- Mounting Position : Any.



3. Maximum Ratings

Electrical Characteristics Rating at 25°C ambient temperature unless otherwise specified.

Characteristic	Symbol	Value	Unit
N-Channel MOSFET			
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	0.34	A
Drain Current - Pulsed(Note1)	I_{DM}	1.36	A
P-Channel MOSFET			
Drain-Source Voltage	V_{DS}	-50	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	-0.18	A
Drain Current - Pulsed(Note1)	I_{DM}	-0.7	A
Power Dissipation, Temperature and Thermal Resistance			
Power Dissipation	P_D	150	mW
Thermal Resistance from Junction to Ambient(Note2)	$R_{\theta JA}$	833	°C/W
Junction Temperature	T_j	-55~+150	°C
Storage Temperature	T_{stg}	-55~+150	°C
Lead Temperature	T_L	260	°C

4. Electrical Characteristics($T_a=25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Condition	Min	TYP	Max	Unit
N-Channel MOSFET						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	60	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 48V, V_{GS} = 0V$	-	-	1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 10	μA
		$V_{GS} = \pm 10V, V_{DS} = 0V$	-	-	± 200	nA
		$V_{GS} = \pm 5V, V_{DS} = 0V$	-	-	± 100	nA
Gate threshold voltage(note 3)	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 1\text{mA}$	1.0	1.3	2.5	V
Drain-source on-resistance(note 3)	$R_{\text{DS(on)}}$	$V_{GS} = 10V, I_D = 500\text{mA}$	-	0.9	5	Ω
		$V_{GS} = 4.5V, I_D = 200\text{mA}$	-	1.1	5.3	
Diode Forward voltage	V_{SD}	$V_{GS} = 0V, I_S = 0.3\text{A}$	-	-	1.5	V
DYNAMIC PARAMETERS (note 4)						
Input Capacitance	C_{iss}	$V_{DS} = 10V, V_{GS} = 0V, f = 1\text{MHz}$	-	-	40	pF
Output Capacitance	C_{oss}		-	-	30	
Reverse Transfer Capacitance	C_{rss}		-	-	10	
SWITCHING PARAMETERS (note 4)						
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD} = 50V, V_{GS} = 10V, R_L = 250\Omega, R_{\text{GEN}} = 50\Omega$	-	-	10	ns
Turn-off delay time	$t_{d(\text{off})}$		-	-	15	
Reverse recovery time	t_{rr}	$I_S = 300\text{mA}; d_I/d_t = -100\text{A/s}; V_{GS} = 0V; V_R = 25V$	-	30	-	ns
Recovered charge	Q_r		-	30	-	
P-Channel MOSFET						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = -250\mu\text{A}$	-50	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -50V, V_{GS} = 0V$	-	-	-15	μA
		$V_{DS} = -25V, V_{GS} = 0V$	-	-	-0.1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 10	μA
Gate threshold voltage(note 3)	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.9	-1.62	-2	V
Drain-source on-resistance(note 3)	$R_{\text{DS(on)}}$	$V_{GS} = -10V, I_D = -0.1\text{A}$	-	4.1	8	Ω
		$V_{GS} = -5V, I_D = -0.1\text{A}$	-	5.5	10	
Forward transconductance (note 3)	g_{FS}	$V_{DS} = -25V, I_D = -0.1\text{A}$	0.05	-	-	S
Diode forward voltage (note 3)	V_{SD}	$V_{GS} = 0V, I_S = 0.13\text{A}$	-	-	-2.2	V
DYNAMIC PARAMETERS (note 4)						
Input Capacitance	C_{iss}	$V_{DS} = -5V, V_{GS} = 0V, f = 1\text{MHz}$	-	30	-	pF
Output Capacitance	C_{oss}		-	10	-	
Reverse Transfer Capacitance	C_{rss}		-	5	-	
SWITCHING PARAMETERS (note 4)						
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD} = -15V, R_L = 50\Omega, I_D = -2.5\text{A}$	-	2.5	-	ns
Turn-on rise time	t_r		-	1	-	
Turn-off delay time	$t_{d(\text{off})}$		-	16	-	
Turn-off fall time	t_f		-	8	-	

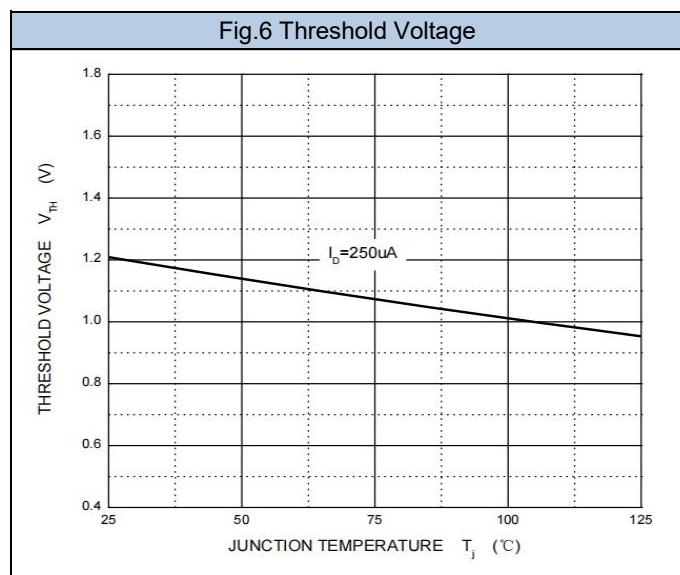
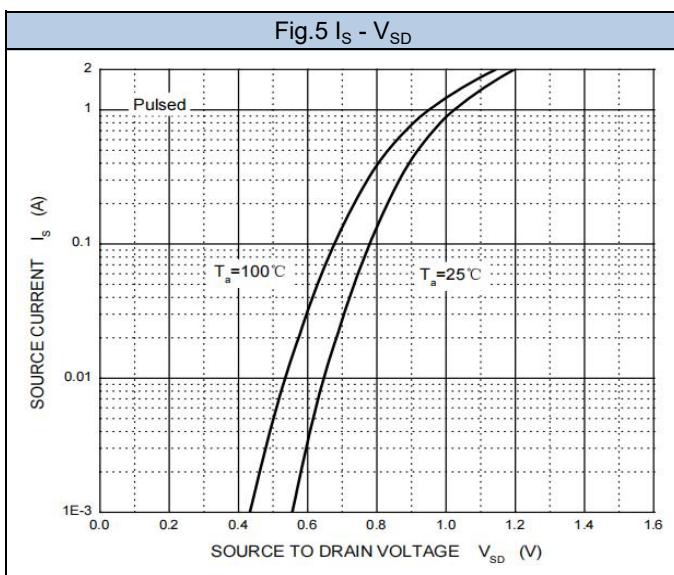
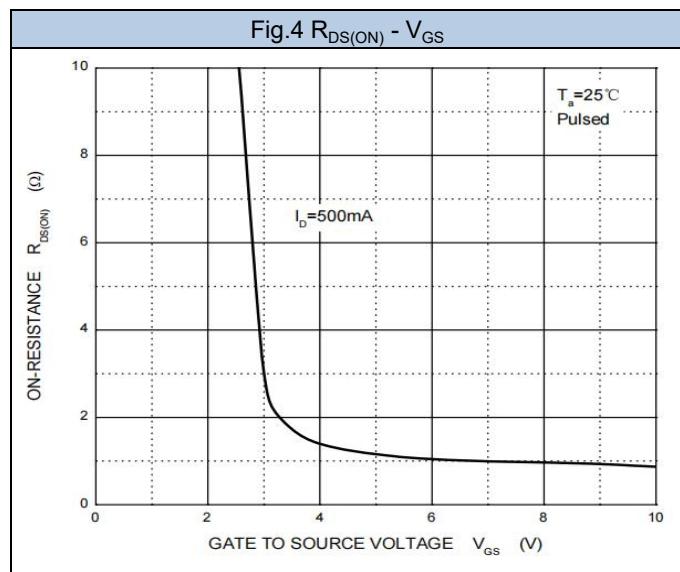
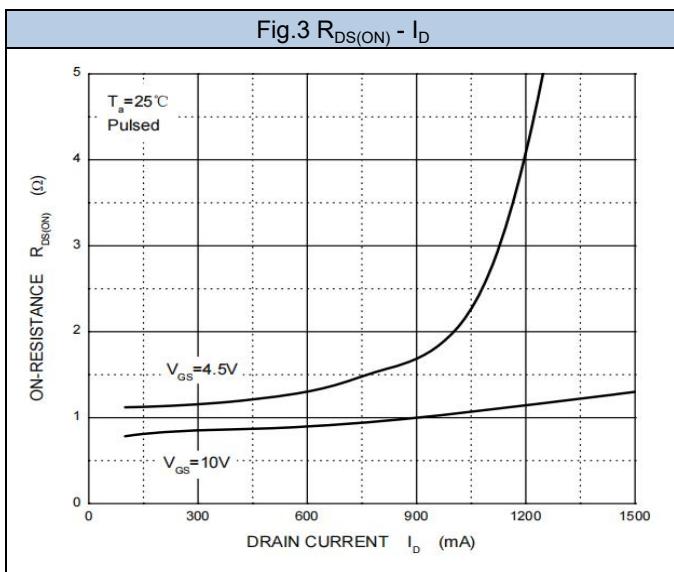
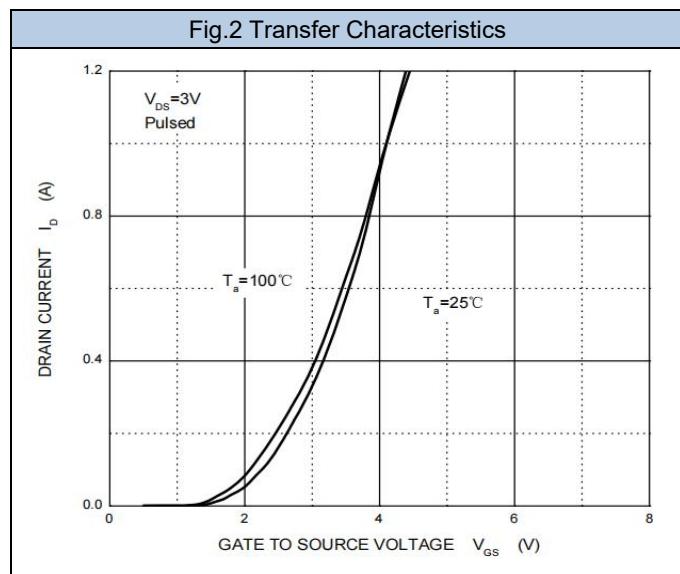
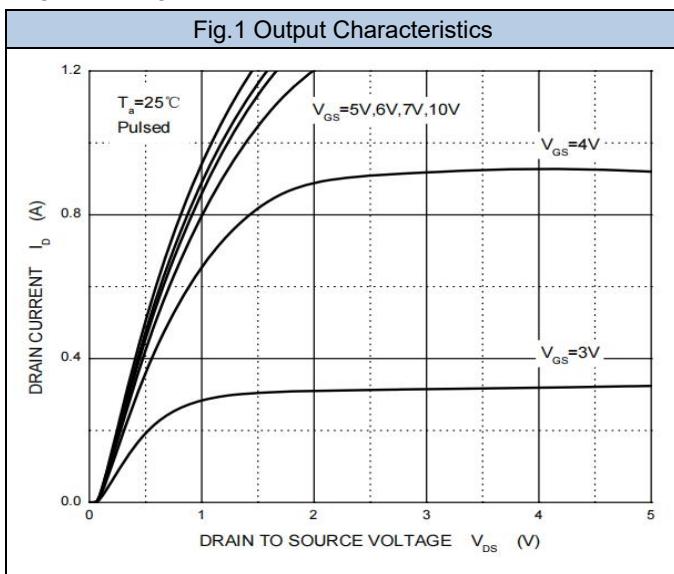
Notes:

1. Surface mounted on FR-4 board using minimum pad size, 1oz copper
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
4. These parameters have no way to verify.



5. Rating And Characteristic Curves

N-Channel MOSFET





P-Channel MOSFET

Fig.1 Output Characteristics

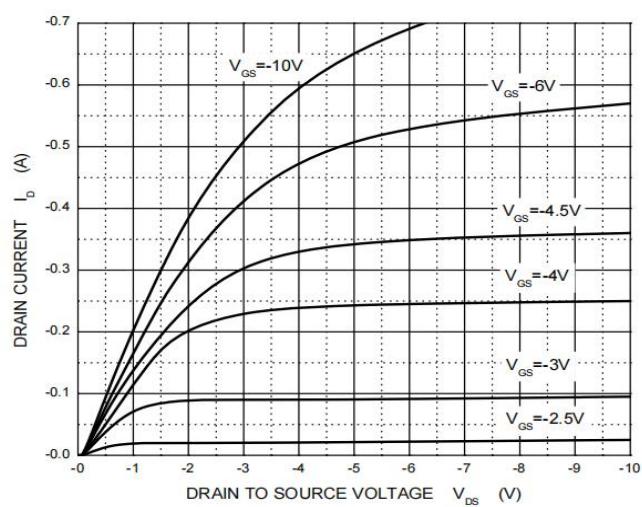


Fig.2 Transfer Characteristics

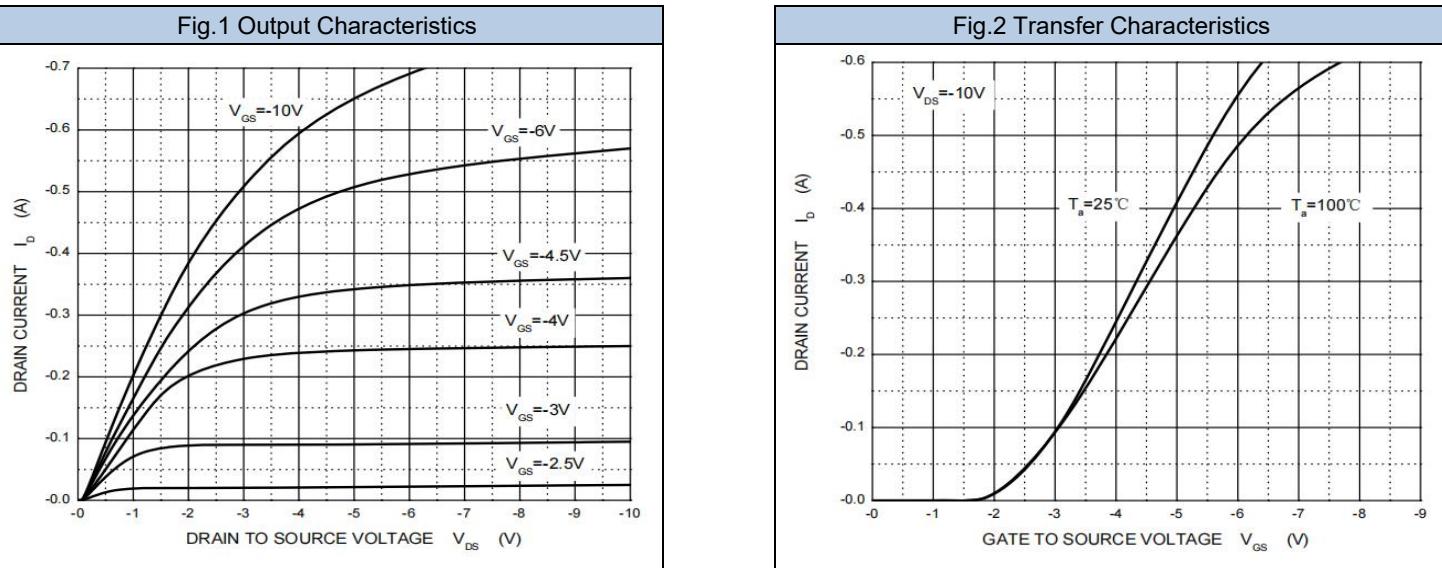
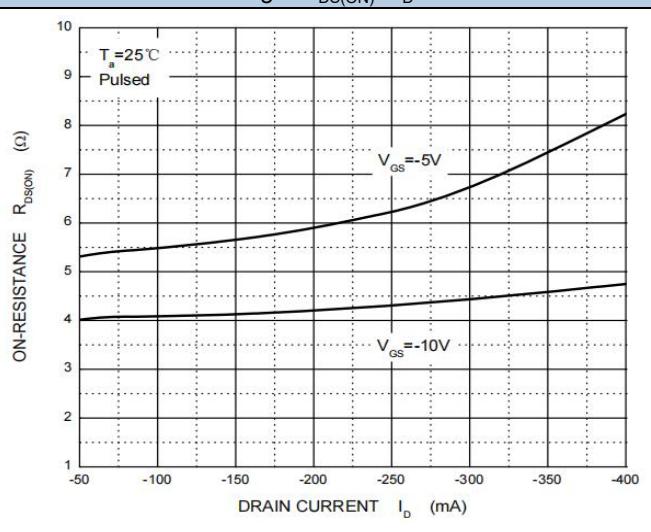
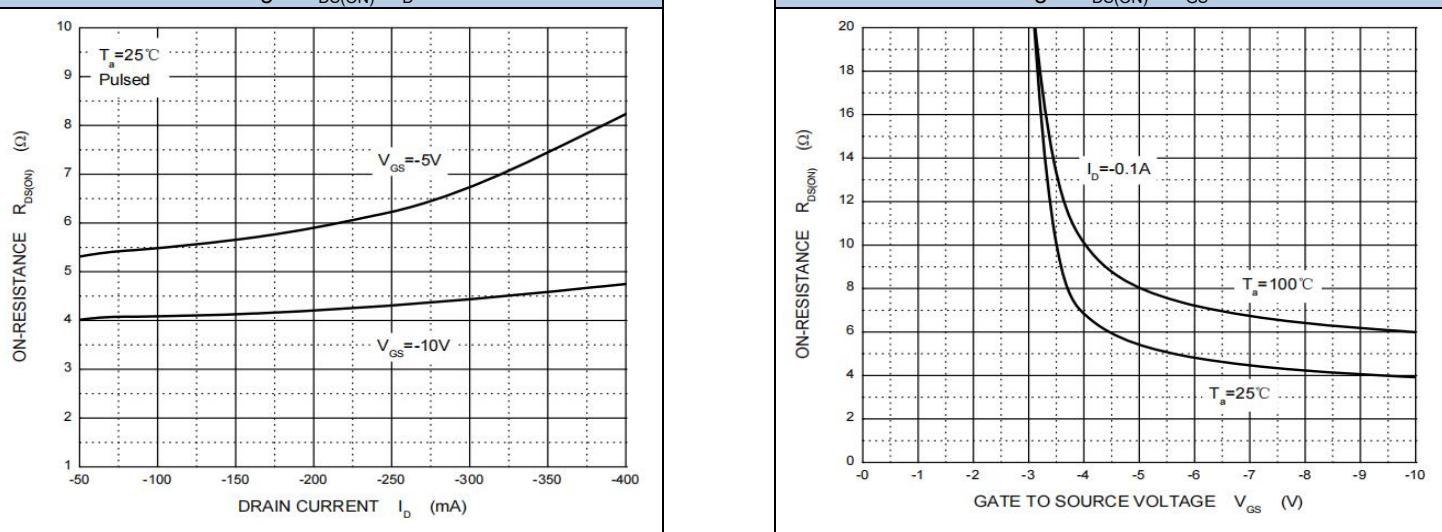
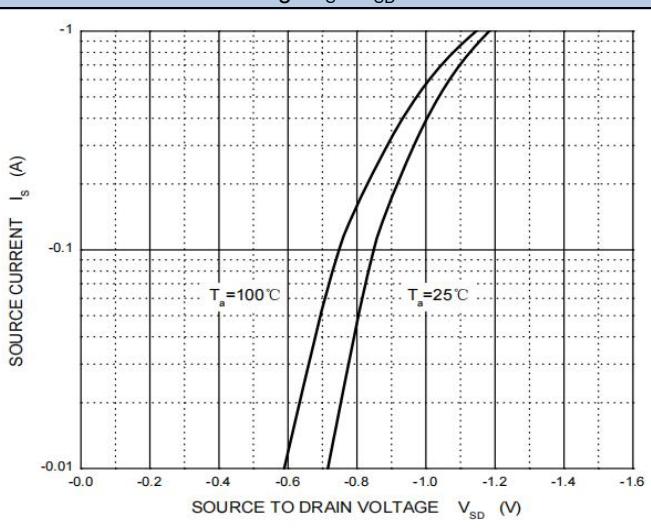
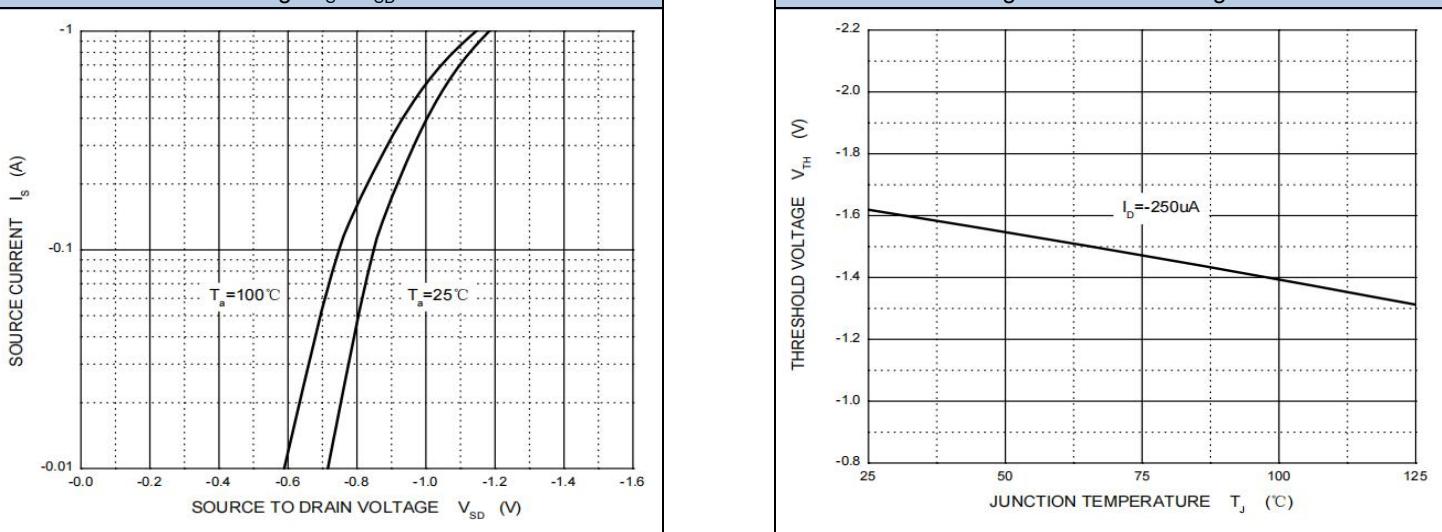
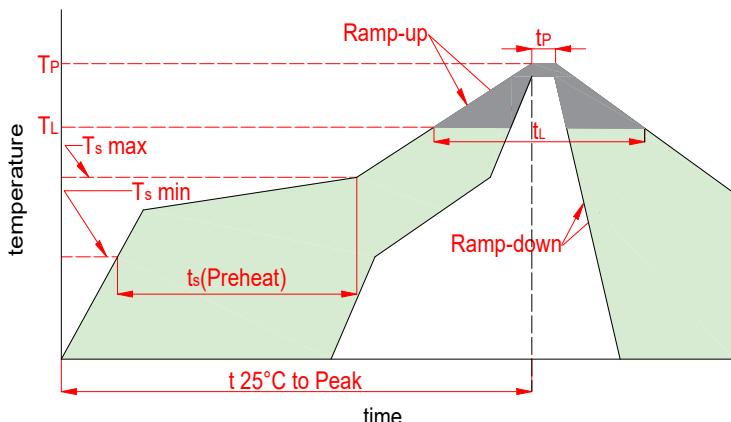
Fig.3 $R_{DS(ON)}$ - I_D Fig.4 $R_{DS(ON)}$ - V_{GS} Fig.5 I_S - V_{SD} 

Fig.6 Threshold Voltage



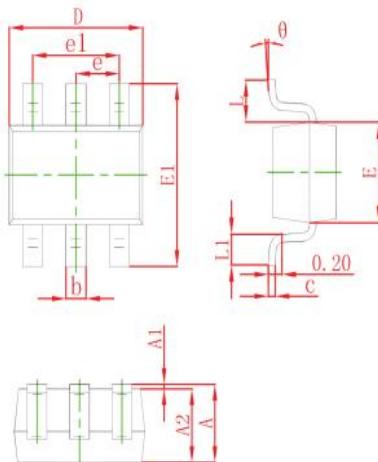


6. Soldering Parameters



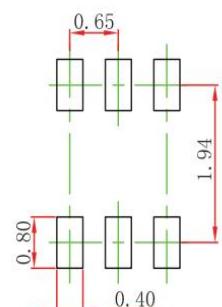
Reflow Condition		Lead-free
Pre Heat	Temp. min(T_s (min))	150°C
	Temp. max(T_s (min))	200°C
	Time(min to max)(t_s)	60~120s
Aver. ramp up rate(Liquidus Temp.)(T_L)to peak		3°C/s max
T_s (max) to T_L -Ramp-up Rate		3°C/s max
Reflow	Temp.(T_L)(Liquidus)	217°C
	Temp.(t_L)(Liquidus)	60~150s
Peak Temp.(T_p)		260 ^{+0/-5} °C
Time within actual peak Temp.(t_p)		30s max
Ramp-down Rate		6°C/s max
Time 25°C to peak Tempe.(T_p)		8 minutes max
Do not exceed		260°C

7. Dimensions

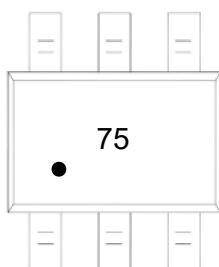


Dimensions	Inches		Millimeters	
	Min	Max	Min	Max
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.010	0.018	0.250	0.450
c	0.003	0.006	0.080	0.150
D	0.079	0.087	2.000	2.200
E	0.045	0.053	1.150	1.350
E1	0.085	0.100	2.150	2.550
e1	0.047	0.055	1.200	1.400
L1	0.010	0.018	0.250	0.460

Mounting PAD Layout



8. Part Marking System



9. Package Information

Package	Part Number	Tape Width(mm)	Quantity(pcs)
SOT-363	XN7252KDW	8	3000



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