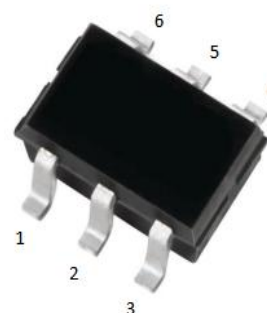




Features

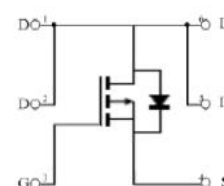
- TrenchFET Power MOSFET
- Excellent RDS(on) and Low Gate Charge

SOT-23-6L



Mechanical Data

- Case: Molded Plastic, SOT-23-6L.
- Epoxy: UL 94V-0 rate flame retardant
- Terminals: Plated Leads Solderable per MIL-STD-750, Method-2026.
- Marking: L4007
- Mounting Position : Any.
- Equivalent Circuit:



Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	-7	A
Pulsed Drain Current ($t=300\mu s$)	I_{DM}	-28	A
Power Dissipation	P_D	1.1	W
Thermal Resistance from Junction to Ambient ($t \leq 10s$)	$R_{\theta JA}$	80	$^{\circ}C/W$
Junction Temperature	T_J	-55~ +150	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}C$

**Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static						
Drain-source breakdown voltage	V _{(BR) DSS}	V _{GS} = 0V, I _D =250μA	-40			V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	-1	-1.6	-2.5	
Gate-source leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} =±20V			±100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} =-40V,V _{GS} = 0V			-1	μA
Drain-source on-resistance ¹	R _{DS(on)}	V _{GS} =-10V, I _D =-5A		28	35	mΩ
		V _{GS} =-4.5V, I _D =-4A		38	50	
Dynamic Characteristics ^{2,3}						
Input capacitance	C _{iss}	V _{DS} =-20V,V _{GS} =0V,f =1MHz		1320		pF
Output capacitance	C _{oss}			116		
Reverse transfer capacitance	C _{rss}			89		
Total gate charge	Q _g	V _{DS} =-20V,V _{GS} =-4.5V,I _D =-10A		23		nC
Gate-source charge	Q _{gs}			2.9		
Gate-drain charge	Q _{gd}			4.3		
Turn-on delay time	t _{d(on)}	V _{DS} =-20V I _D =-1A V _{GS} =-10V,R _G =6Ω		12.8		ns
Turn-on rise time	t _r			8.7		
Turn-off delay time	t _{d(off)}			65		
Turn-off fall time	t _f			12.6		
Source-drain diode characteristics and maximum ratings						
Diode forward voltage	V _{SD}	V _{GS} =0V,I _S =-1A		-0.72	-1.4	V

Note :

1. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
2. Guaranteed by design, not subject to production testing.
3. These parameters have no way to verify.



Rating And Characteristic Curves

Fig1: Output Characteristics

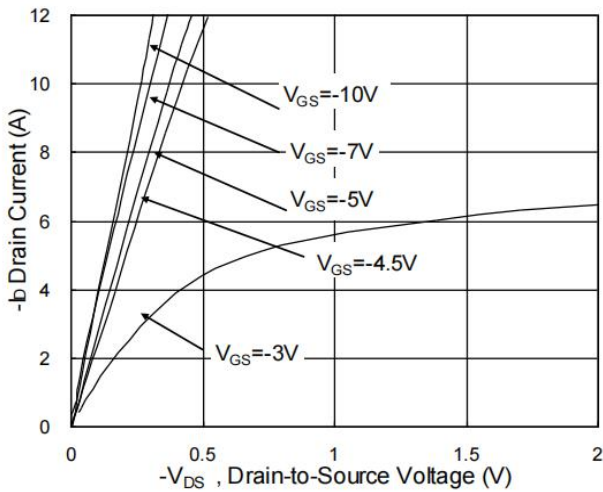


Fig2: Gate Charge Characteristics

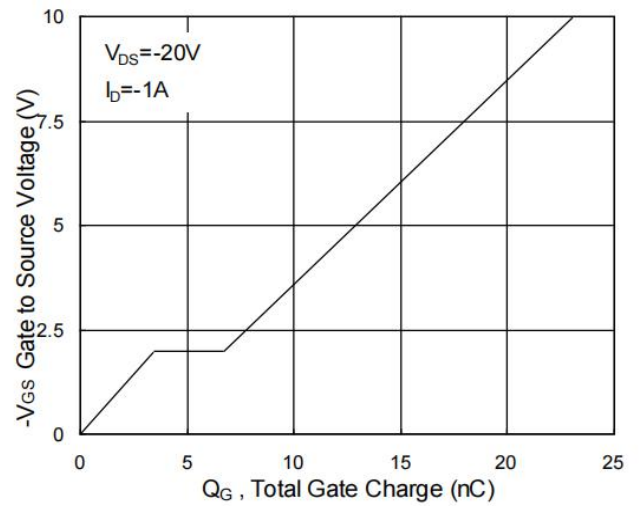


Fig.3 Safe Operating Area

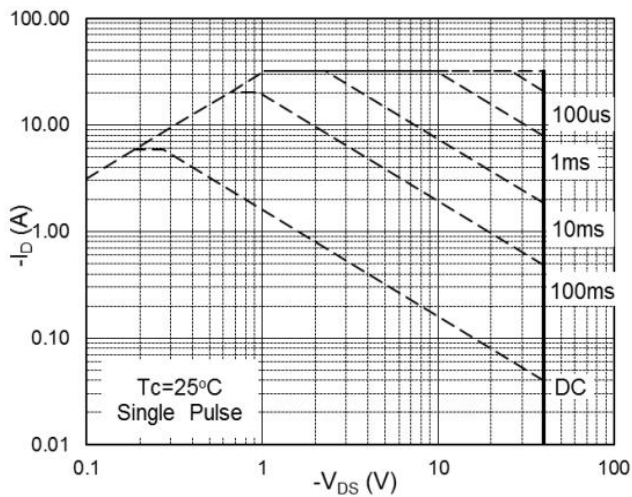


Fig4: $R_{DS(ON)}$ - V_{GS}

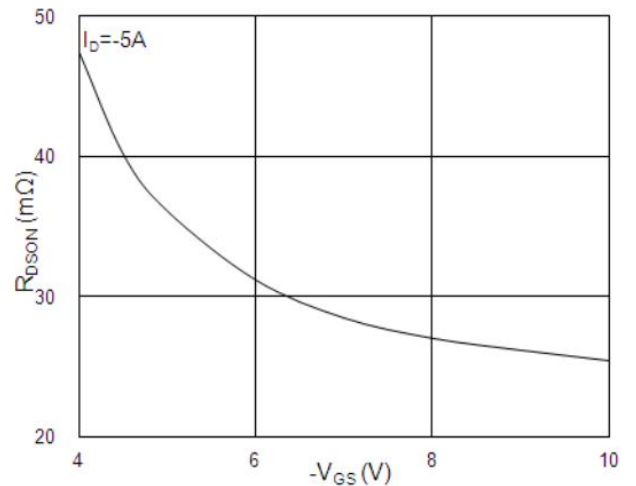


Fig.5 I_S - V_{SD}

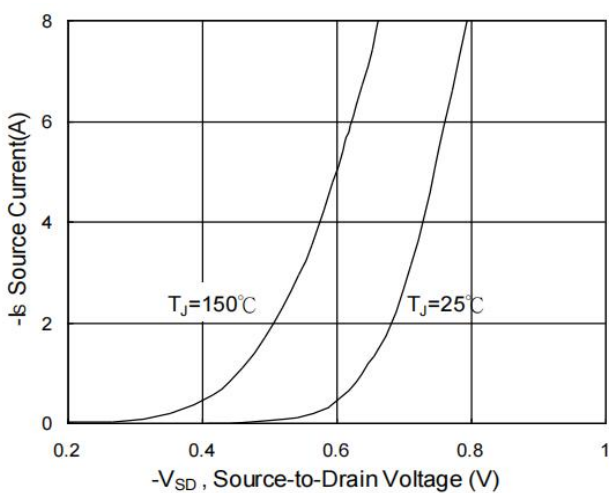
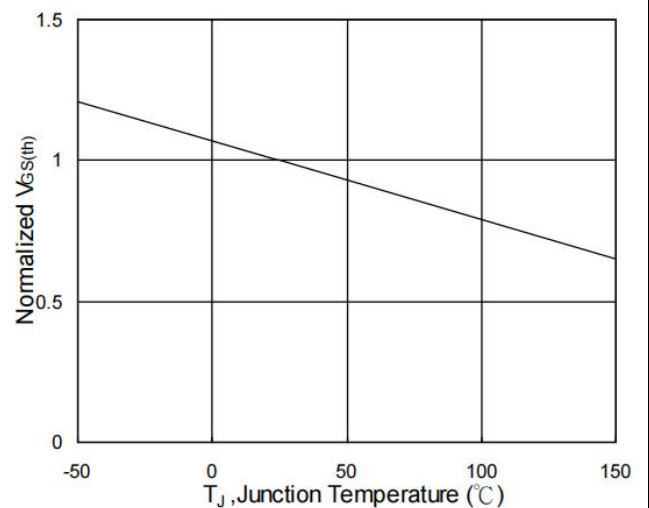
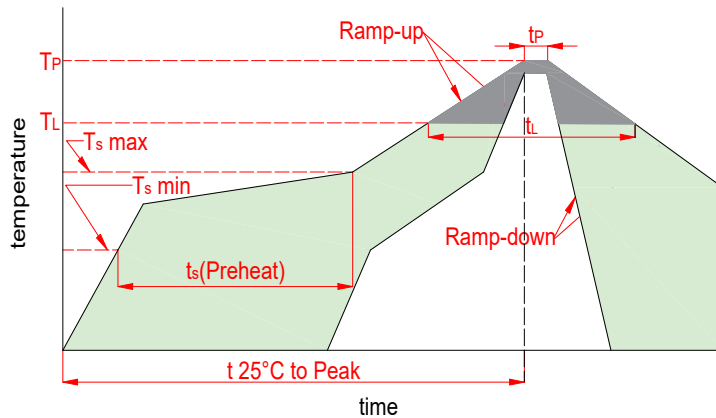


Fig.6 Threshold Voltage



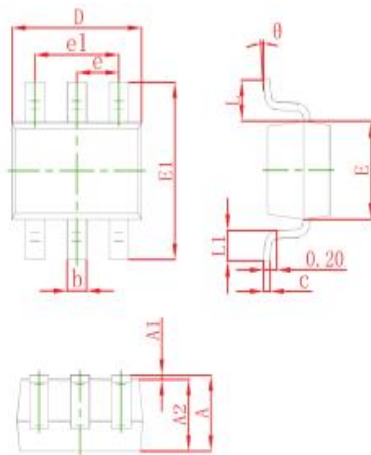


6. Soldering Parameters



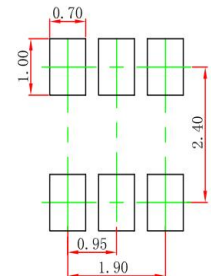
Reflow Condition		Lead-free
Pre Heat	Temp. min(T_s (min))	150℃
	Temp. max(T_s (min))	200℃
	Time(min to max)(t_s)	60~120s
Aver. ramp up rate(Liquidus Temp.)(T_L)to peak		3℃/s max
T_s (max) to T_L -Ramp-up Rate		3℃/s max
Reflow	Temp. (T_L)(Liquidus)	217℃
	Temp. (t_L)(Liquidus)	60~150s
Peak Temp. (T_P)		260 ^{+0/-5} ℃
Time within actual peak Temp. (t_p)		30s max
Ramp-down Rate		6℃/s max
Time 25℃ to peak Tempe. (T_P)		8 minutes max
Do not exceed		260℃

7. Dimensions

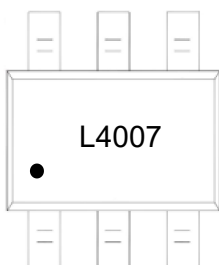


Dimensions	Inches		Millimeters	
	Min	Max	Min	Max
A	0.041	0.057	1.050	1.450
A1	0.000	0.006	0.000	0.150
A2	0.041	0.051	1.050	1.300
b	0.012	0.020	0.300	0.500
c	0.004	0.008	0.100	0.200
D	0.106	0.122	2.700	3.100
E	0.055	0.071	1.400	1.800
E1	0.102	0.118	2.600	3.000
e1	0.071	0.079	1.800	2.000
L1	0.012	0.024	0.300	0.600

Mounting PAD Layout



8. Part Marking System



9. Package Information

Package	Part Number	Tape Width(mm)	Quantity(pcs)
SOT-23-6L	XN4007	8	3000



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