



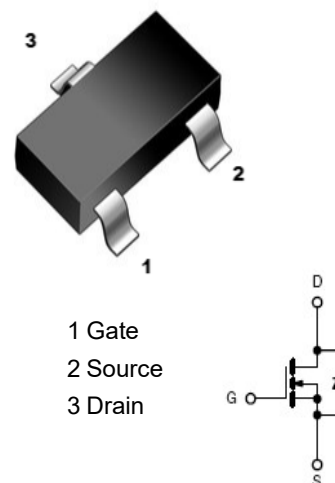
1. Features

- High density cell design for extremely low RDS(on)
- Rugged and Reliable

2. Mechanical Data

- Case:Molded Plastic,SOT-23 .
- Epoxy:UL 94V-0 rate flame retardant
- Terminals:Plated Leads Solderable perMIL-STD-750,Method-2026.
- Marking:SS
- Mounting Position : Any.

SOT-23



3. Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	UNIT
Drain-Source Voltage	V_{DS}	50	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	0.34	A
Drain Current-Pulsed	I_{DM}	1.4	A
Power Dissipation	P_D	0.35	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	357	°C/W
Junction and Storage Temperature Range	T_J, T_{STG}	-55~ +150	°C

**4. Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Off Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	50			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 50V, V_{GS} = 0V$			0.5	μA
		$V_{DS} = 30V, V_{GS} = 0V$			100	nA
Gate-source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
On characteristics ¹						
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 0.22A$		0.8	3.0	Ω
		$V_{GS} = 4.5V, I_D = 0.22A$		0.85	5.0	Ω
Forward tranconductance	g_{FS}	$V_{DS} = 10V, I_D = 0.22A$		0.13		S
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.8	1.1	1.5	V
Dynamic Characteristics ²						
Input capacitance	C_{iss}	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$		41.9		pF
Output capacitance	C_{oss}			9.1		pF
Reverse transfer capacitance	C_{rss}			5.0		pF
Switching Characteristics ^{1,2}						
Turn-on delay time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 30V, I_D = 0.29A, R_G = 6\Omega$			5	ns
Turn-on rise time	t_r				18	ns
Turn-off delay time	$t_{d(off)}$				36	ns
Turn-off fall time	t_f				14	ns
Source-drain diode characteristics and maximum ratings						
Diode forward voltage ¹	V_{SD}	$I_S = 0.44A, V_{GS} = 0V$		1.15	1.4	V

Note :

1. Pulse Test ; Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
2. These parameters have no way to verify.



5. Rating And Characteristic Curves

Fig1: Output Characteristics

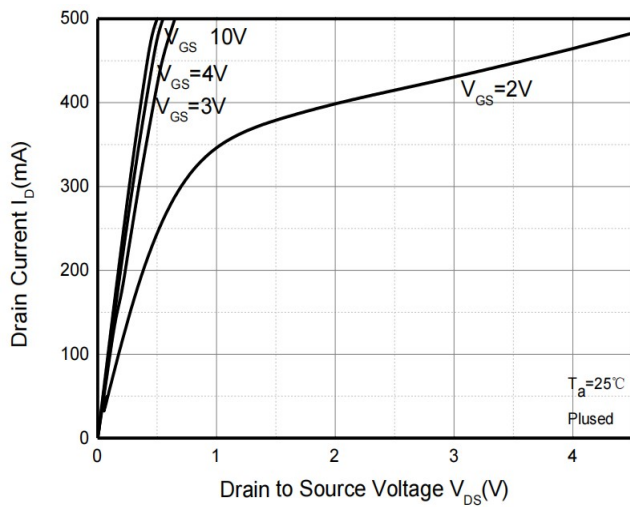


Fig2: Transfer Characteristics

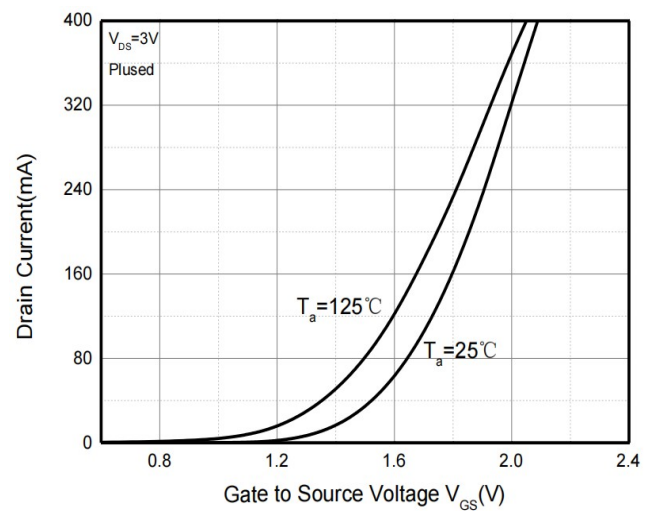


Fig.3 $R_{DS(on)} - I_D$

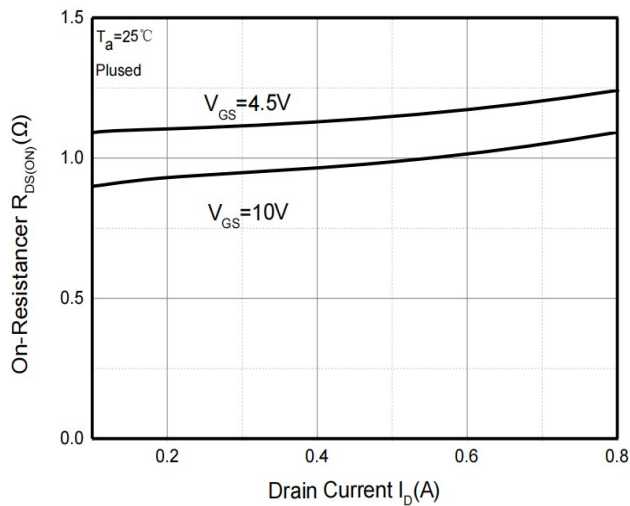


Fig.4 $R_{DS(on)} - V_{GS}$

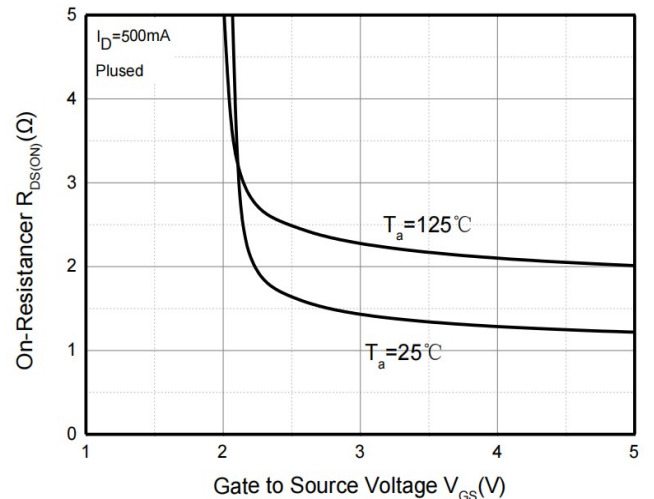


Fig.5 IS - VSD

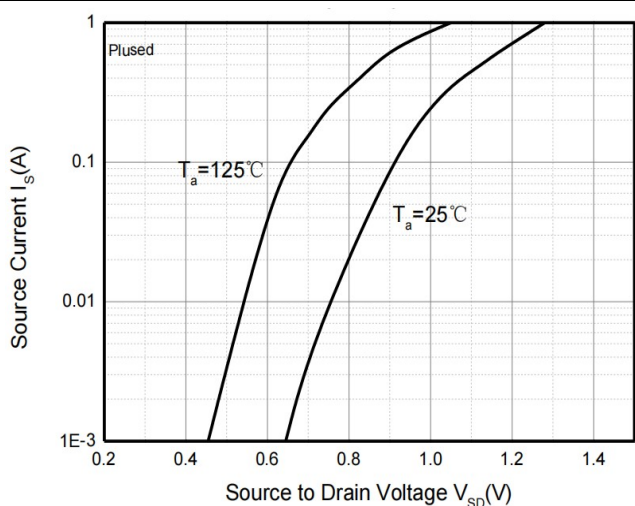
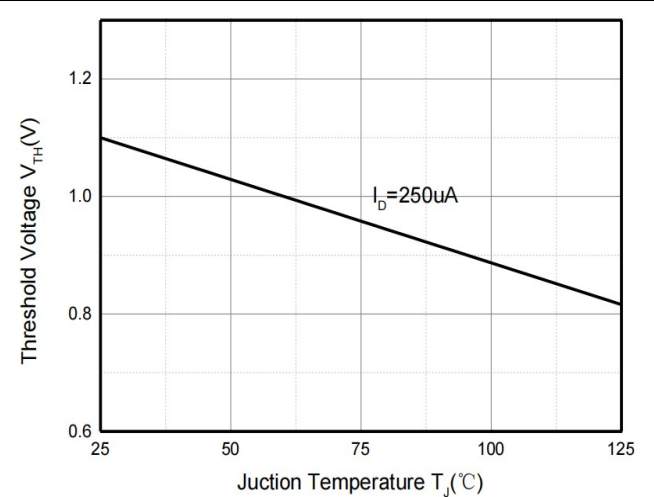
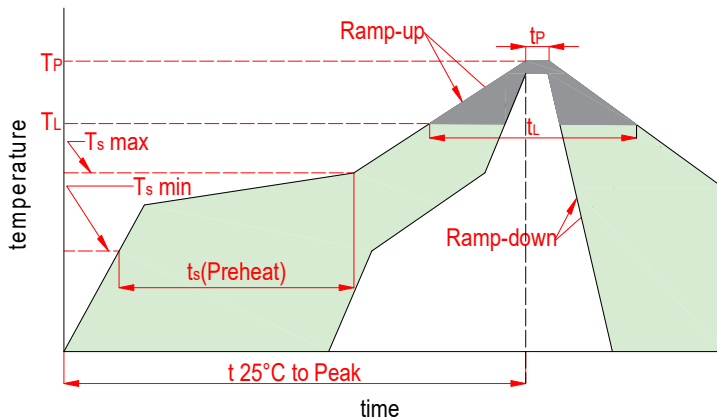


Fig.6 Threshold Voltage



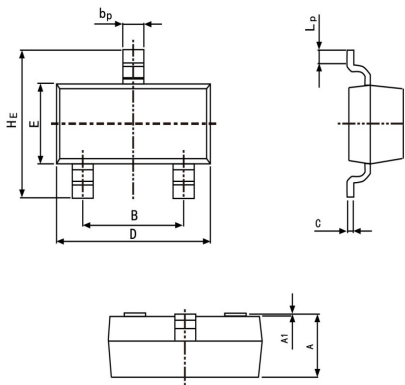


6. Soldering Parameters



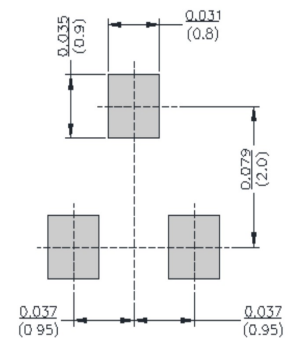
Reflow Condition		Lead-free
Pre Heat	Temp. min(T_s (min))	150℃
	Temp. max(T_s (min))	200℃
	Time(min to max)(t_s)	60~120s
Aver. ramp up rate(Liquidus Temp.)(T_L)to peak		3℃/s max
T_s (max) to T_L -Ramp-up Rate		3℃/s max
Reflow	Temp.(T_L)(Liquidus)	217℃
	Temp.(t_L)(Liquidus)	60~150s
Peak Temp.(T_P)		260 ^{+0/-5} ℃
Time within actual peak Temp.(t_p)		30s max
Ramp-down Rate		6℃/s max
Time 25℃ to peak Tempe.(T_P)		8 minutes max
Do not exceed		260℃

7. Dimensions

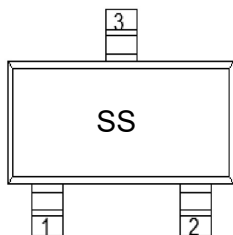


Dimensions	Inches		Millimeters	
	Min	Max	Min	Max
A	0.035	0.045	0.90	1.15
B	0.070	0.081	1.78	2.05
bp	0.012	0.020	0.30	0.51
C	0.003	0.007	0.08	0.18
D	0.110	0.118	2.80	3.00
E	0.047	0.055	1.20	1.40
HE	0.087	0.110	2.20	2.80
A1	0.000	0.004	0.00	0.10
LP	0.008	0.020	0.20	0.50

Mounting PAD Layout



8. Part Marking System



9. Package Information

Package	Part Number	Tape Width(mm)	Quantity(pcs)
SOT-23	BSS138	8	3000



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