



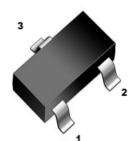


1. Features

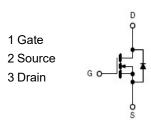
- Surface Mount Package
- High Density Cell Design for Extremely Low RDS(ON)
- Voltage Controlled Small Signal Switch
- Rugged and Reliable

2. Mechanical Data

- Case:Molded Plastic,SOT-23.
- Epoxy:UL 94V-0 rate flame retardant
- Terminals:Plated Leads Solderable perMIL-STD-750,Method-2026.
- Marking: 03N10
- Mounting Position : Any.



SOT-23



3. Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	UNIT
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	$V_{\rm GS}$	±20	V
Continuous Drain Current ^{1,2}	I _D	3	Α
Drain Current-Pulsed(tp=10µs)	I _{DM}	12	Α
Power Dissipation	P _D	1.25	W
Thermal Resistance from Junction to Ambient ^{1,2}	R _{eJA}	100	°C/W
Junction Temperature	T _J	-55~ +150	${\mathbb C}$
Storage Temperature	T _{STG}	-55~ +150	${\mathbb C}$



4. Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

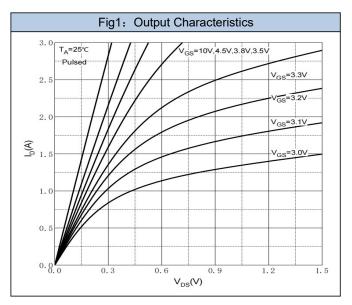
Parameter	Symbol	Test Condition		Тур	Max	Units
Off Characteristics						
Drain-source breakdown voltage	V(BR) DSS	oss Vgs = 0V, Ip =250µA				V
Zero gate voltage drain current	ldss	V _{DS} =80V,V _{GS} = 0V			1	μA
Gate-source leakage current	Igss	Vgs =±20V, Vps = 0V			±100	nA
On characteristics ³						
Drain-source on-resistance	Dans,	Vgs =10V, ID =1A		110	140	mΩ
	RDS(on)	Vgs =4.5V, I _D =1A		140	190	mΩ
Forward tranconductance	gFS	V _{DS} =5V, I _D =3A		5		S
Gate threshold voltage	Vgs(th)	V _{DS} =V _{GS} , I _D =250μA 1		1.5	3.0	V
Dynamic Characteristics						
Input capacitance	Ciss			142.4		pF
Output capacitance	Coss	V _{DS} =45V,V _{GS} =0V,f =1MHz		56.18		pF
Reverse transfer capacitance	Crss			4.45		pF
Total gate charge	Qg			3.29		nC
Gate-source charge	Qgs	V _{DS} =50V,V _{GS} =10V,I _D =1A		0.21		nC
Gate-drain charge	Qgd			1.06		nC
Switching Characteristics						
Turn-on delay time	td(on)			6		ns
Turn-on rise time	tr	V_{GS} =10V, V_{DD} =50V,		4		ns
Turn-off delay time	td(off)	$R_G=19\Omega$, $R_L=3\Omega$		20		ns
Turn-off fall time	tf			4		ns
Source-drain diode characteristics and maximum ratings						
Diode forward voltage ³	V_{SD}	Is=1A,VGs=0V		0.8	1.2	V

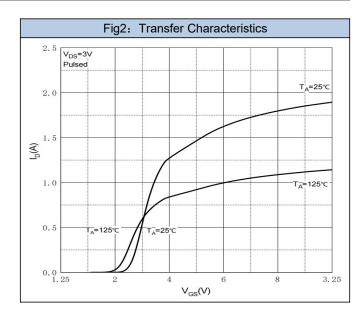
Note:

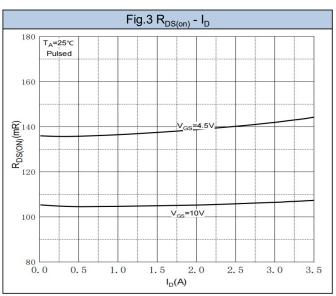
- 1. R θ JA is measured with the device mounted on 1 in2 FR4 board with 1oz. single side copper, in a still air environment with TA = 25°C.
- 2. $R\theta JA$ is measured in the steady state
- 3. Pulse test : Pulse width \leq 380µs, duty cycle \leq 2%.

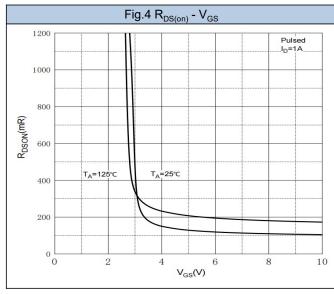


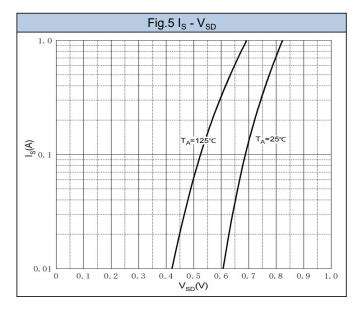
5. Rating And Characteristic Curves

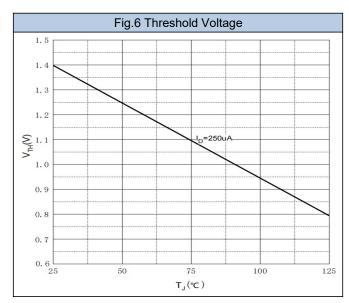






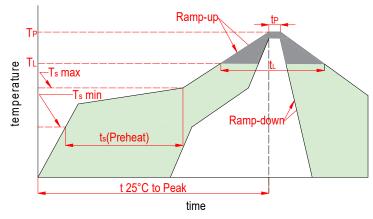






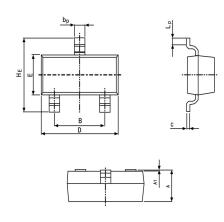


6. Soldering Parameters

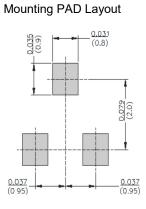


Reflow Condition		Lead-free	
	Temp. min(T _s (min))	150℃	
Pre Heat	Temp. max(T _s (min))	200℃	
	Time(min to max)(t₅)	60~120s	
Aver. ram	p up rate(Liquidus Temp.)(T _L)to peak	3℃/s max	
T _s (max) to T _L -Ramp-up Rate		3℃/s max	
Reflow	Temp.(T _L)(Liquidus)	217℃	
	Temp.(t _L)(Liquidus)	60~150s	
Peak Tem	np.(T _P)	260 ^{+0/-5} ℃	
Time with	in actual peak Temp.(t _p)	30s max	
Ramp-down Rate		6℃/s max	
Time 25℃ to peak Tempe.(T _p)		8 minutes max	
Do not exceed		260℃	

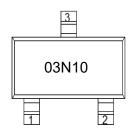
7. Dimensions



Dimensions	Inches		Millimeters		
Dimensions	Min	Max	Min	Max	
Α	0.035	0.045	0.90	1.15	
В	0.070	0.081	1.78	2.05	
bp	0.012	0.020	0.30	0.51	
С	0.003	0.007	0.08	0.18	
D	0.110	0.118	2.80	3.00	
E	0.047	0.055	1.20	1.40	
HE	0.087	0.110	2.20	2.80	
A1	0.000	0.004	0.00	0.10	
LP	0.008	0.020	0.20	0.50	



8. Part Marking System



9. Package Information

Package	Part Number	Tape Width(mm)	Quantity(pcs)
SOT-23	XN03N10	8	3000



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