



KMB12F THRU KMB125F

Single Phase 1.0 AMP Surface Mount Schottky Bridge Rectifier

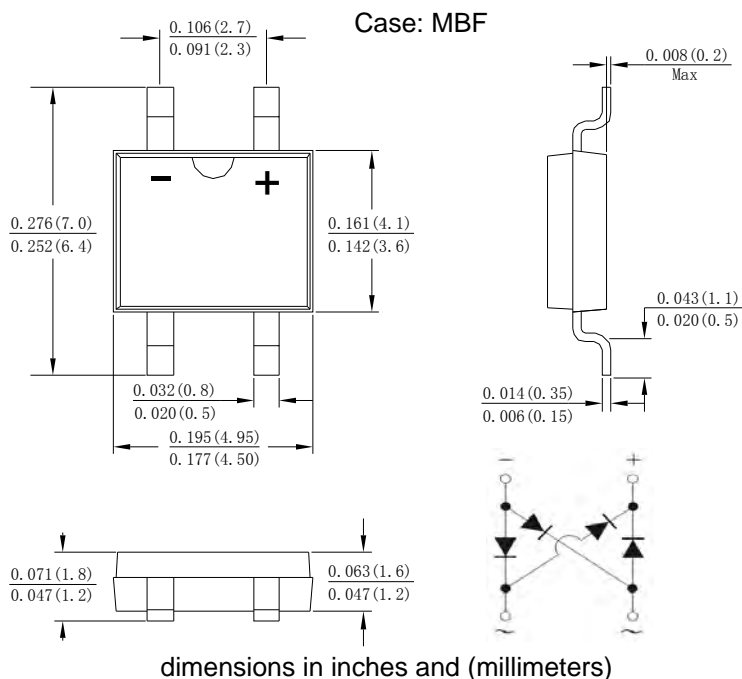
Features

- Schottky Brrier Chip
- Low Power Loss,High Efficiency
- Ideally Suited for Automatic Assembly
- Surge Overload Rating to 30A Peak
- Plastic Case Material has UL Flammability

Classification Rating 94V-0

Mechanical Data

- Case: MB-F, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any
- Marking: type number
- Lead Free: For RoHS / Lead Free Version,



Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	KMB 12F	KMB 13F	KMB 14F	KMB 145F	KMB 15F	KMB 16F	KMB 18F	KMB 110F	KMB 115F	KMB 120F	KMB 125F	UNITS	
Peak Repetitive Reverse Voltage	V _{RRM}	20	30	40	45	50	60	80	100	150	200	250		
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	31	35	42	56	70	105	140	175	V	
DC Blocking Voltage	V _{DC}	20	30	40	45	50	60	80	100	150	200	250		
Average Rectified Output Current (Note1) @T _C = 100°C	I _{F(AV)}	1.0											A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	30											A	
I ² t Rating for Fusing (t < 8.3ms)	I ² t	3.735											A²s	
Forward Voltage per element @I _F =1.0A	V _{FM}	0.55				0.7		0.85		0.90		0.92	V	
Peak Reverse Current @T _J = 25°C At Rated DC Blocking Voltage @T _J = 100°C	I _{RM}	0.1						0.05						mA
		10						5						
Typical Junction Capacitance (Note2)	C _j	50						35						pF
Typical Thermal Resistance	R _{θJL}	16											°C/W	
Operating junction temperature range	T _J	-55 to +150											°C	
Operating and Storage Temperature Range	T _{STG}	-55 to +150											°C	

Note:

1. Mounted on aluminum substrate PC board with 1.3mm² solder pad.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



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Fig. 1 Output Current Derating Curve

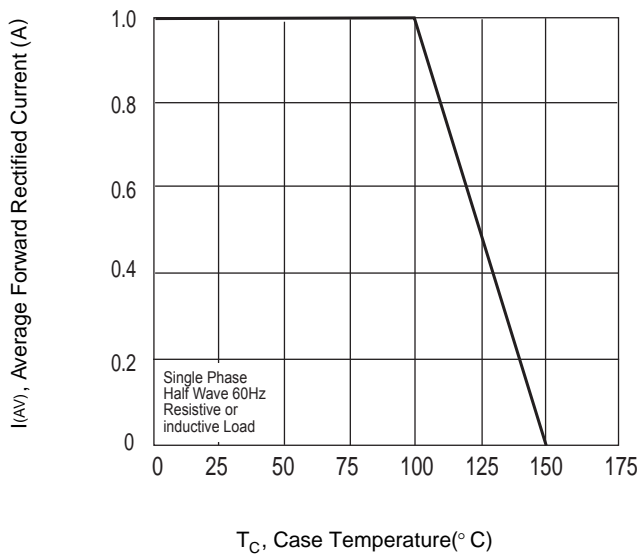


Fig. 2 Typical Forward Characteristics

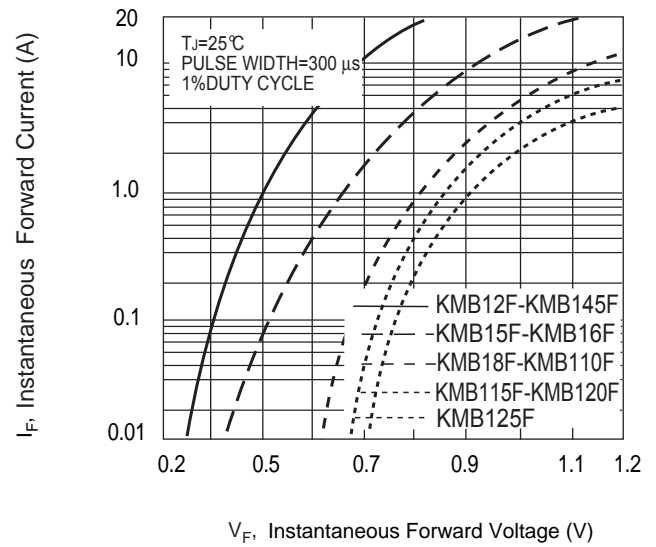


Fig.3 Maximum Peak Forward Surge Current

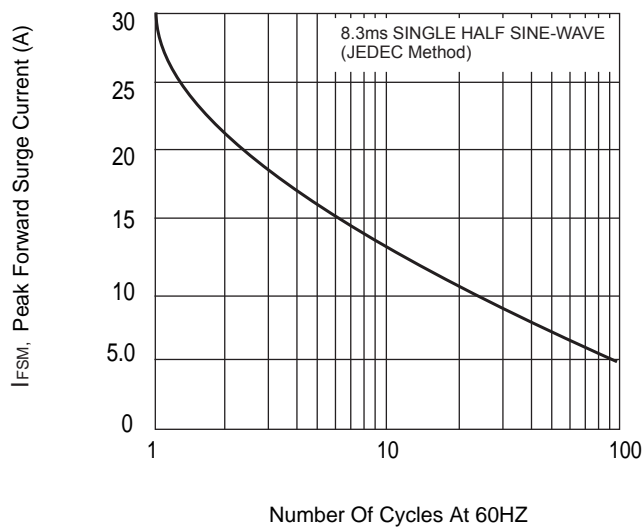


Fig. 4 Typical Junction Capacitance

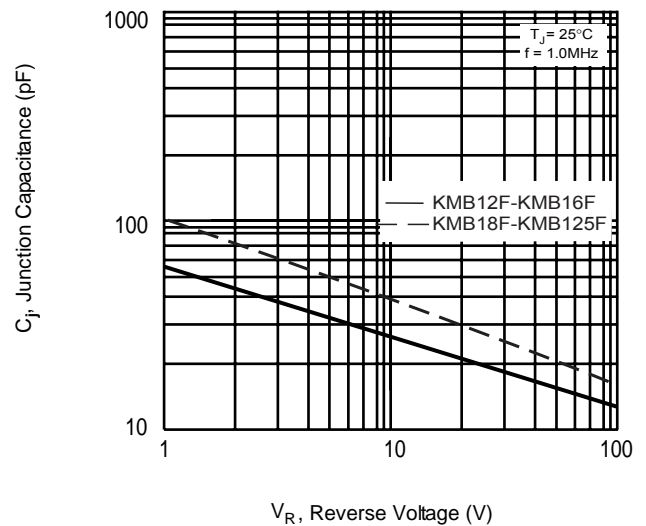


Fig. 5 Typical Reverse Characteristics

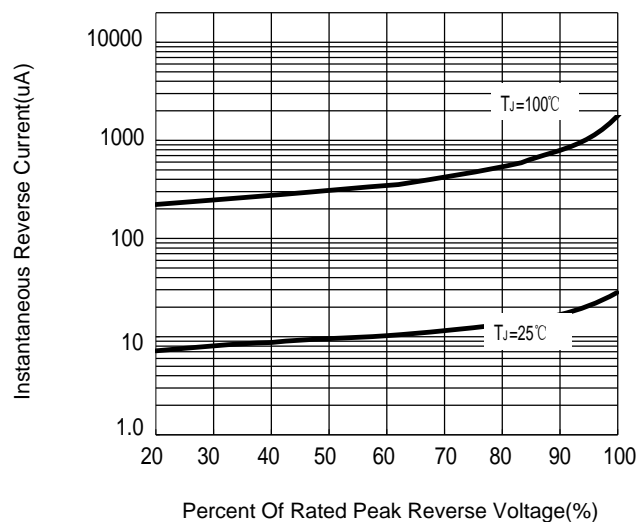
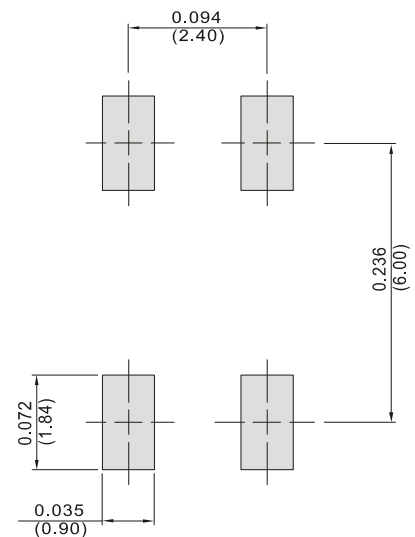


Fig. 6 Mounting Pad Layout





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