



ABS21N THRU ABS210N

Single Phase 2.0AMP Surface Mount Glass Passivated Bridge Rectifier

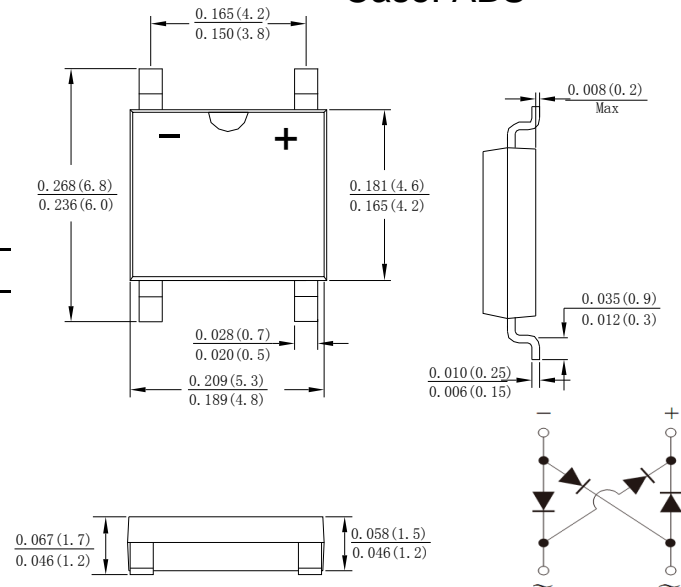
Features

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Designed for surface mount application
- Plastic material-UL flammability 94V-0

Mechanical Data

- Case: SOPA-4, molded plastic ABS
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any
- Marking: type number

Case: ABS



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	ABS21N	ABS22N	ABS24N	ABS26N	ABS28N	ABS210N	UNITS
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM}	100	200	400	600	800	1000	V
	V _{RWM}							
	V _{DC}							
RMS Reverse Voltage	V _{RMS}	70	140	280	420	560	700	V
Average Rectified Output Current (Note:1) @T _c =100 °C	I _{F(AV)}	2.0						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	50						A
I ² t Rating for Fusing (t < 8.3ms)	I ² t	10.375						A ² s
Forward Voltage per element @I _F =1.0A @I _F =2.0A	V _{FM}	0.98 1.1						V
Peak Reverse Current @T _J =25 °C At Rated DC Blocking Voltage @T _J =125 °C	I _R	5.0 100						uA
Typical Junction Capacitance (Note2)	C _J	20						pF
Typical Thermal Resistance	R _{θJA}	62.5						°C/W
	R _{θJL}	25						
Operating and Storage Temperature Range	T _J , T _{STG}	-55to+150						°C

Note:1. Mounted on glass epoxy PC board with 1.3mm² solder pad.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



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Fig. 1 Output Current Derating Curve

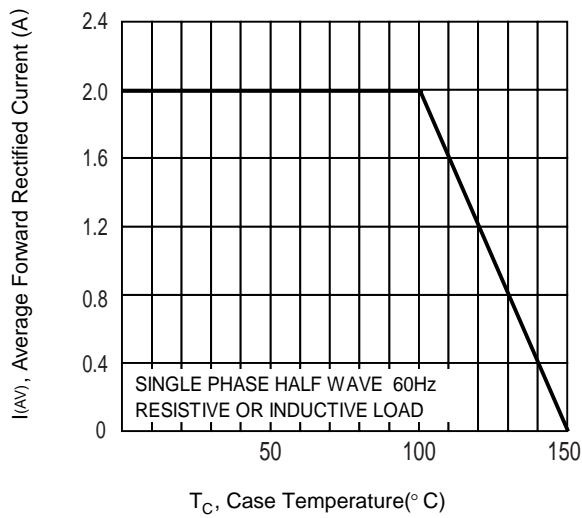


Fig. 2 Typical Forward Characteristics

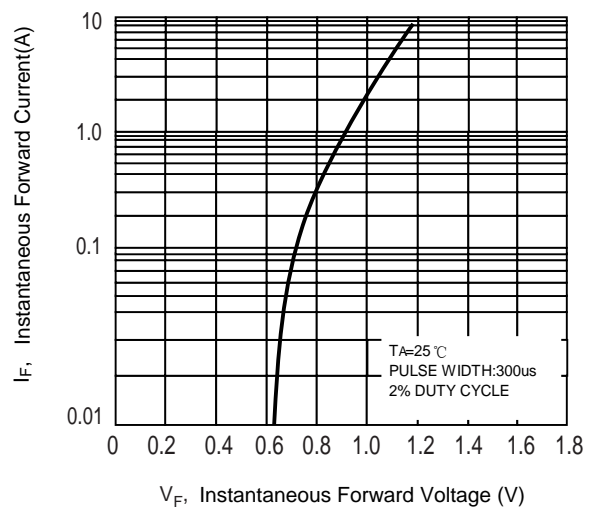


Fig.3 Maximum Peak Forward Surge Current

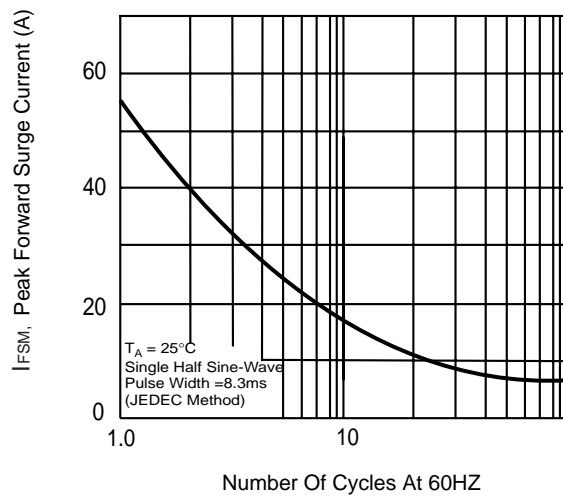


Fig.4 Typical Reverse Characteristics

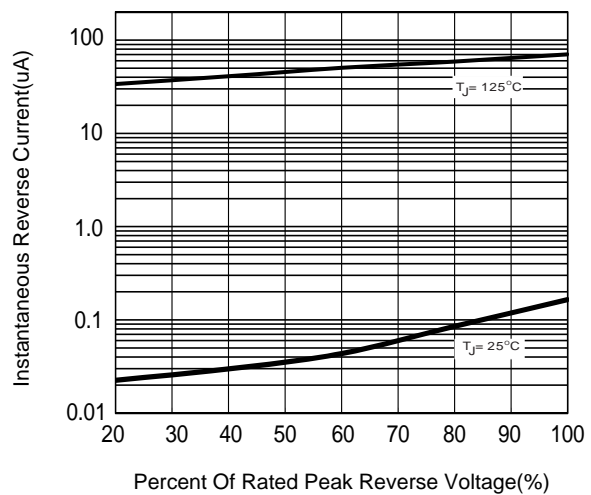


Fig. 5 Typical Junction Capacitance

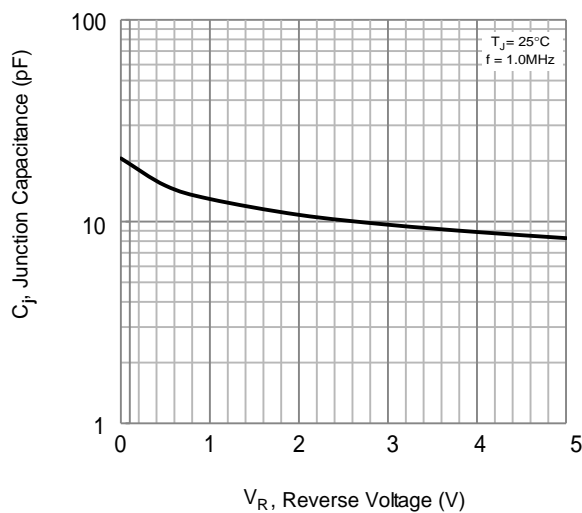
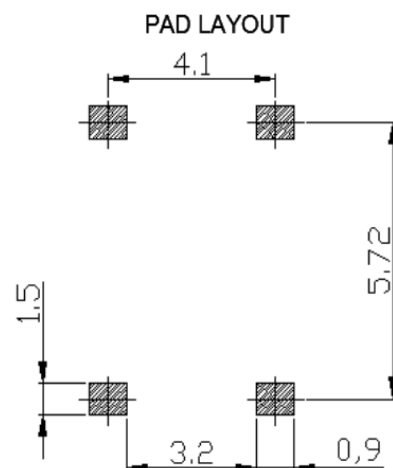


Fig.6 Mounting Pad Layout





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